Table XX – Allowed Terminal\_type Associations

| **Terminal\_type** | **Terminal\_type\_qualifier** | **aggressor** |
| --- | --- | --- |
| **pin\_name** | **signal\_name** | **bus\_label** | **pad\_name** |
| Buf\_I/O | I/O |  |  |  | Aggressor |
| Buf\_PU\_Ref | I/O |  |  |  |  |
| Buf\_PD\_Ref | I/O |  |  |  |  |
| Buf\_PC\_Ref | I/O |  |  |  |  |
| Buf\_GC\_Ref | I/O |  |  |  |  |
| Buf\_Ext\_Ref | I/O |  |  |  |  |
| Buf\_Rail |  | Sig | Bus |  |  |
| Pad\_I/O | I/O |  |  |  | Aggressor |
| Pad\_Rail |  | Sig | Bus | Die |  |
| Pin\_I/O | I/O |  |  |  | Aggressor |
| Pin\_Rail | Rail | Sig | Bus |  |  |

Notes

 “I/O” refers to I/O pin names.

 “Rail” refers to POWER or GND pin names.

 “Sig” refers to POWER or GND signal names.

 “Bus” refers to bus labels.

 “Die” refers to Die Supply Pads.

 “Aggressor” refers to connections that have incomplete Aggressors.

Blank cells indicate that the qualifier/Terminal\_type is not allowed