**DesignCon 2020 IBIS Summit**

January 31, 2020
Santa Clara, CA

**Agenda**

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| 8:00 AM | **REFRESHMENTS AND SIGN IN**  |
| 8:30 AM | **OFFICIAL OPENING** - Welcome to Summit - Introductions  |
| 8:45 AM | **IBIS Chair’s Report** Randy Wolff (Micron Technology, USA)  |
| 9:15 AM | **IBIS-ATM Task Group Report** Arpad Muranyi (Mentor, a Siemens Business, USA)  |
| 9:30 AM | **IEEE 2401-2019 Publication with Supporting IBIS Version 7.0** Kazunari Koga (Zuken (for JEITA), Japan)  |
| 10:00 AM | **BREAK AND REFRESHMENTS**  |
| 10:20 AM | **DDR Simulation with IBIS-AMI** Randy Wolff, Justin Butterfield (Micron Technology, USA)[Presented by Randy Wolff (Micron Technology, USA)]  |
| 10:50 AM | **IBIS-AMI Modeling and Simulation of DDR5 Systems** Fangyi Rao\*, Hee-Soo Lee\*, Jing-Tao Liu\*, Wendem Beyene\*\* (Keysight Technologies\*, Intel Corp.\*\*, USA) [Presented by Fangyi Rao (Keysight Technologies, USA)] |
| 11:20 AM | **IBIS-AMI & COM Co-design for 25G Serdes** Nan Hou#, Amy Zhang#, Guohua Wang#, David Zhang#, Anders Ekholm## (Ericsson, PRC#, Sweden##) [Presented by Anders Ekholm (Ericsson, Sweden)] |
| 12:00 PM | **FREE LUNCH** - Pre-registration required  |
| 1:00 PM | **Gap in IBIS for Sampling with Statistical Mode AMI Models** Todd Bermensolo\*, Hansel Dsilva\*\*, Michael Mirmak (Keysight Technologies\*, Achronix Semiconductor Corp.\*\*, USA) [Presented by Todd Bermensolo (Keysight Technologies, USA)]  |
| 1:45 PM | **IBIS AMI Back-Channel System Optimization in Practice**Steven Parker\*, Matthew Kelly\*, Jared James\*\*, Ambrish Varma\*\*,Kumar Keshavan\*\*, Ken Willis\*\*(Marvell\*, Cadence Design Systems\*\*, USA)[Presented by Steven Parker\*, Jared James\*\*(Marvell\*, Cadence Design Systems\*\*, USA)] |
| 2:15 PM | **BIRD201 – Back-channel Statistical Optimization** Walter Katz, Eric Brock (The MathWorks, USA)[Presented by Walter Katz (The MathWorks, USA)] |
| 2:45 PM | **BREAK AND REFRESHMENTS**  |
| 3:05 PM | **Use Data Science Techniques in IBIS-AMI Modeling** Wei-hsing Huang (SPISim, USA)  |
| 3:30 PM | **The On Die Decap Modeling Proposal (BIRD198)** Atsushi Tomishima\*, Megumi Ono\*\* (for JEITA) (Toshiba Electronic Devices & Storage Corporation\*, Socionext\*\*, Japan)[Presented by Genichi Tanaka (Renesas Electronics, Japan) |
| 4:00 PM | **EMD Made Simple** Bob Ross (Teraspeed Labs, USA) |
| 4:25 PM | **IBIS Based Buck Converter DC Modeling** Zhiping Yang, Songping Wu, Shuai Jin, Zhenxue Xu (Google, USA) [Presented by Shuai Jin, Zhenxue Xu (Google, USA)]  |
| 4:55 PM | **OPEN DISCUSSION AND CONCLUDING ITEMS** - Next Open Forum Meeting: February 21, 2020  |
| 5:00 PM | **END OF MEETING ROOM AVAILABILITY**  |

