DATe European IBIS Summit
March the 11th, 2005

ZUKEN

customized engineering solutions

DATe 2005

ICM Messe Munich,
Munich, Germany

SIEMENS

cadence

Agenda I

8:30   REFRESHMENTS & SIGN IN

9:00   WELCOME AND INTRODUCTIONS
   - Ralf Bruening, Zuken, Germany

9:10   SIG - SIEMENS IBIS GROUP
   - Eckhard Lenski, Siemens AG, Germany

**9:20   CAN WE STOP THE DISPARITY OF THE POTENTIAL OF IBIS ?
   (MODEL PARAMETERS AND THE REALITY OF DELIVERED
       PARAMETERS)**
   - Eckhard Lenski, Siemens AG, Germany

9:45   MODIFYING IBIS FILES
   - Katja Koller, Siemens AG, Germany

**10:10   AN INITIAL CASE STUDY FOR BIRD95 - ENHANCING IBIS FOR SSO
   POWER   INTEGRITY SIMULATION**
   **- Sam Chitwood, Raymond Chen, and Jiayuan Fang, Sigrity, Inc. USA
       (Presented by Jiayuan Fang)**

10:35   BREAK (10 Minutes)

Agenda II

10:45 IBIS IN THE DESIGN CHAIN OF NOISE MODELLING
- Manfred Maurer, Siemens AG, and Thomas Steinecke, Infineon AG Technologies, Germany

11:10 THE ROLE OF IBIS IN NEAR-FIELD EMISSION PREDICTION OF ICS
- Etienne Sicard and Alexandre Boyer, National Institute of Applied Science (INSA), France; and Gilles Peres, EADS Airbus Industries, France
  (Presented by Etienne Sicard)

11:35 COMPUTER-ASSISTED MODELING OF DIGITAL I/O BUFFERS FOR IBIS
- Flavio Canavero, Ivan Maio, and Igor Stievano, Politecnico di Torino, Italy; Madhavan Swaminathan, Georgia Institute of Technology, USA; and Paul Franzon, North Carolina State University, USA
  (Presented by Igor Stievano)

12:00 LUNCH/DISCUSSION (40 Minutes)

Agenda III

12:00 LUNCH/DISCUSSION (40 Minutes)

12:40 CONSIDERATION OF SWITCHING CHARACTERISATION
- Michael Schaeder, Zuken, Germany

13:05 PRE/DE-EMPHASIS BUFFER MODELING WITH IBIS
- Arpad Muranyi, Intel Corporation, USA

13:40 HIGH SPEED MODELS, THE FIRST STEP TOWARDS HIGH SPEED DESIGN KITS
- Rainer Asfalg, Mentor Graphic Germany

14:10 MODELLING COMPLEX IO WITH IBIS 4.1
- Heiko Dudek, Cadence Design Systems/FlowCad, Germany

14:45 CLOSING TOPICS/DISCUSSION
- Ralf Bruening, Zuken

15:00 END OF MEETING