

**AGENDA – Virtual European IBIS Summit
Virtual SPI2021 Workshop
Wednesday, May 12, 2021**

Room: **Virtual**
Sponsors: **IBIS Open Forum**

*(Order and times subject to change)
Times are CEST (Central European Summer Time)*

- 16:00 **SIGN IN, MEETING WELCOME**
- 16:10 **IBIS Chair's Report**
Wolff, Randy (Micron Technology, USA)
- 16:25 **Power Integrity Analysis for High Speed ASICs**
Meng, Liqiang; Liu, Xiaoxuan (ZTE Corporation, China)
[Presented by Liu, Xiaoxuan (ZTE Corporation, China)]
- 16:50 **IEEE Transactions on Signal and Power Integrity**
Duffy, Alistair (De Montfort University, UK)
(President, IEEE EMC Society)
- 17:15 **Time-Domain Macromodel Extraction**
Ross, Bob (Teraspeed Labs, USA)
- 17:40 **SPICE Macromodel Generation**
Ross, Bob (Teraspeed Labs, USA)
- 18:00 **BREAK (Reconvene at 18:10)**
- 18:10 **Recent Developments on Advanced Macromodeling by Politecnico di Torino**
Bradde, Tommaso; De Stefano, Marco; Zanco, Alessandro; Grivet-Talocia, Stefano
(Politecnico di Torino, Italy)
*[Presented by Grivet-Talocia, Stefano; De Stefano, Marco; Zanco, Alessandro;
Bradde, Tommaso (Politecnico di Torino, Italy)]*
- 18:50 **Frequency-Dependent Per-Port Renormalization**
Chen, Sherman*; Xu, Zhifei** (Kandou Bus, *UK, **Switzerland)
[Presented by Chen, Sherman (Kandou Bus, UK)]
- 19:20 **A PAM-4 Behavioral Model using Laguerre-Volterra Feed Forward Neural Network and its Implementation in IBIS-AMI**
Wang, Xinying; Nguyen, Thong; Shutt-Ainé, José (University of Illinois, Urbana, USA)
[Presented by Wang, Xinying (University of Illinois, Urbana, USA)]
- 19:50 **CONCLUDING ITEMS**
- 20:00 **END OF IBIS SUMMIT MEETING**
- Thank you for your participation