

AGENDA - Asian IBIS Summit (Shanghai) Friday, November 1, 2019

Parkyard Hotel
Shanghai, P.R. China

Room: **Ballroom ABC**

Sponsors: **Huawei Technologies (Primary)**
ANSYS
Cadence Design Systems
Keysight Technologies
Synopsys
ZTE Corporation

(Order and times subject to change)

09:00 **SIGN IN**

- Vendor Tables Open at 9:00

09:30 **MEETING WELCOMES**

- Yan, Hang (Paul)
(Huawei Technologies, PRC)
- Wolff, Randy (Chair, IBIS Open Forum)
(Micron Technology, USA)

09:40 **IBIS Chair's Report**

Wolff, Randy (Micron Technology, USA)

10:00 **How to Obtain Buffer Impedance from IBIS**

Wang, Lance (Zuken, USA)

10:20 **C-PHY SI Simulation with IBIS Model**

Zhang, Bailing (ANSYS, PRC)

10:35 **BREAK**

- Refreshments and Vendor Tables
- Reconvene at 10:55

10:55 **Innovations in DDR Memory Simulation**

Slater, Stephen (Keysight Technologies, USA)

[Presented by Zhao, Jiajie (Keysight Technologies, PRC)]

11:20 **IBIS-AMI and COM Co-design for 25G Serdes**

Hou*, Nan; Zhang*, Amy; Wang*, Guohua; Zhang**, David; Ekholm**, Anders
(Ericsson, *PRC, **Sweden)

[Presented by Hou, Nan (Ericsson, PRC)]

12:00 **FREE LUNCH (hosted by sponsors)**

- Reconvene at 13:30
- 13:30 **How to Fix a Short Channel Problem with AMI and COM Simulation**
Ye, Dongdong; Zhu, Shunlin (ZTE Corporation, PRC)
[Presented by Ye, Dongdong (ZTE Corporation, PRC)]
- 14:00 **Celestica 112G SI Channel Study for 800G Switch**
Shi, Bowen; Feng, Sophia (Celestica, PRC)
[Presented by Shi, Bowen (Celestica, PRC)]
- 14:30 **Channel Simulation Over DDR4/5 and Above**
Keshavan*, Kumar; Varma*, Ambrish; Willis*, Ken; Liang**, Skipper
(Cadence Design Systems, *USA, **ROC)
[Presented by Skipper Liang (Cadence Design Systems, ROC)]
- 15:05 **CONCLUDING ITEMS**
- 15:10 **END OF IBIS SUMMIT MEETING**
 - Thank you for your participation
- 15:10 **BREAK**
 - Refreshments and Vendor Tables
- 15:30 **VENDOR PRESENTATIONS, MODERATOR**
Lance Wang (Vice-chair, IBIS Open Forum)
(Zuken, USA)
- 17:30 **END OF VENDOR PRESENTATIONS**
 - Thank you to our sponsors

