

AGENDA - Asian IBIS Summit (Taipei)
Monday, November 4, 2019

Sherwood Hotel
Taipei, Taiwan

Room: **Ballroom 3F** (3rd Floor, look for signs)

Sponsors: **ANSYS**
Cadence Design Systems
Synopsys

(Order and times subject to change)

09:00 **SIGN IN**

- Vendor Tables Open at 9:00

09:30 **MEETING WELCOME**

Randy Wolff (Chair, IBIS Open Forum)
(Micron Technology, USA)

09:45 **IBIS Chair's Report**

Randy Wolff (Micron Technology, USA)

10:05 **Introducing IBIS Version 7.0**

Michael Mirmak*, Randy Wolff**
(*Intel Corporation, **Micron Technology; USA)
[Presented by Randy Wolff (Micron Technology, USA)]

10:30 **BREAK**

- Refreshments and Vendor Tables
- Reconvene at 10:50

10:50 **How to Obtain Buffer Impedance from IBIS**

Lance Wang (Zuken, USA)

11:20 **IBIS-AMI and COM Co-design for 25G Serdes**

Nan Hou*; Amy Zhang*; Guohua Wang*; David Zhang**; Anders Ekholm**
(Ericsson, *PRC, **Sweden)
[Presented by Anders Ekholm (Ericsson, Sweden)]

12:00 **FREE LUNCH (hosted by sponsors)**

- Reconvene at 13:30

13:30 **Innovations in DDR Memory Simulation**

Stephen Slater (Keysight Technologies, USA)
[Presented by Nash Tu (Keysight Technologies, Taiwan)]

14:10 **Channel Simulation Over DDR4/5 and Above**

Kumar Keshavan*, Ambrish Varma*, Ken Willis*, Skipper Liang**

(Cadence Design Systems, *USA, **Taiwan)
[Presented by Skipper Liang (Cadence Design Systems, Taiwan)]

14:40 **IBIS File Format Links**

Bob Ross (Teraspeed Labs, USA)

[Presented by Randy Wolff (Micron Technology, USA)]

15:10 **CONCLUDING ITEMS**

15:15 **END OF IBIS SUMMIT MEETING**

- Thank you for your participation

15:15 **BREAK**

- Refreshments and Vendor Tables

15:35 **VENDOR PRESENTATIONS, MODERATOR**

Lance Wang (Vice-chair, IBIS Open Forum)
(Zuken, USA)

16:50 **END OF VENDOR PRESENTATIONS**

- Thank you to our sponsors

