### 3.1 Keyword Hierarchy

## ibs FILE

- File Header Section
  - [IBIS Ver]
  - [Comment Char]
  - [File Name]
  - [File Rev]
  - [Date]
  - [Source]
  - [Notes]
  - [Disclaimer]
  - [Copyright]

- [Component] Si_location, Timing_location
  - [Manufacturer]
  - [Package] R_pkg, L_pkg, C_pkg
  - [Pin] signal_name, model_name, R_pin, L_pin, C_pin
  - [Package Model]
    - [Alternate Package Models]
      - [End Alternate Package Models]
  - [Pin Mapping] pulldown_ref, pullup_ref, gnd_clamp_ref, power_clamp_ref, ext_ref
  - [Diff Pin] inv_pin, vdiff, tdelay_typ, tdelay_min, tdelay_max
  - [Series Pin Mapping] pin_2, model_name, function_table_group
  - [Series Switch Groups] On, Off
  - [Node Declarations]
    - [End Node Declarations]
  - [Circuit Call] Signal_pin, Diff_signal_pins, Series_pins, Port_map
    - [End Circuit Call]
  - [Begin EMI Component] Domain, Cpd, C_Heatsink_gnd, C_Heatsing_float
    - [Pin EMI] domain_name, clock_div
    - [Pin Domain EMI] percentage
    - [End EMI Component]
  - [Model Selector]

- [Model] Model_type, Polarity, Enable, Vinl, Vinh, C_comp, C_comp_pullup, C_comp_pulldown, C_comp_power_clamp, C_comp_gnd_clamp, Vmeas, Cref, Rref, Vref, Rref_diff, Cref_diff
  - [Model Spec] Vinh, Vinl, Vinh+, Vinh-, Vinl+, Vinl-, S_overshoot_high, S_overshoot_low, D_overshoot_high, D_overshoot_low, D_overshoot_time, D_overshoot_area_h, D_overshoot_area_l, D_overshoot_ampl_h, D_overshoot_ampl_l, Pulse_high, Pulse_low, Pulse_time, Vmeas, Cref, Rref, Cref_rising, Cref_falling, Rref_rising, Rref_falling, Vref_rising, Vref_falling, Vmeas_rising, Vmeas_falling, Rref_diff, Cref_diff

- [Receiver Thresholds] Vth, Vth_min, Vth_max, Vinh_ac, Vinh_dc, Vinl_ac, Vinl_dc, Threshold_sensitivity, Reference_supply, Vcross_low, Vcross_high, Vdiff_ac, Vdiff_dc, Tslew_ac, Tdiffslew_ac
- [Add Submodel]
- [Driver Schedule]
- [Temperature Range]
- [Voltage Range]
- [Pullup Reference]
- [Pulldown Reference]
- [POWER Clamp Reference]
- [GND Clamp Reference]
- [External Reference]
- [TTgnd]
- [TTpower]
- [Pulldown]
- [Pullup]
- [GND Clamp]
- [POWER Clamp]
- [ISSO PU]
- [ISSO PD]
- [Rgnd]
- [Rpower]
- [Rac]
- [Cac]
- [On]
- [Off]
- [R Series]
- [L Series]
- [Rl Series]
- [C Series]
- [Lc Series]
- [Rc Series]
- [Series Current]
- [Series MOSFET] Vds
- [Ramp] dV/dt_r, dV/dt_f, R_load
- [Rising Waveform] R_fixture, V_fixture, V_fixture_min, V_fixture_max, C_fixture, L_fixture, R_dut, L_dut, C_dut
  - [Composite Current]
- [Falling Waveform] R_fixture, V_fixture, V_fixture_min, V_fixture_max, C_fixture, L_fixture, R_dut, L_dut, C_dut
  - [Composite Current]
- [Test Data] Test_data_type, Driver_model, Driver_model_inv, Test_load
  - [Rising Waveform Near]
  - [Falling Waveform Near]
  - [Rising Waveform Far]
  - [Falling Waveform Far]

        - [Diff Rising Waveform Near]
        - [Diff Falling Waveform Near]
        - [Diff Rising Waveform Far]
        - [Diff Falling Waveform Far]
        - [Test Load] Test_load_type, C1_near, Rs_near, Ls_near, C2_near, Rp1_near, Rp2_near, Td, Zo, Rp1_far, Rp2_far, C2_far, Ls_far, Rs_far, C1_far, V_term1, V_term2, Receiver_model, Receiver_model_inv, R_diff_near, R_diff_far
    - [External Model] Language, Corner, Parameters, Ports, D_to_A, A_to_D
        - [End External Model]
    - [Algorithmic Model] Executable
        - [End Algorithmic Model]
    - [Begin EMI Model] Model_emi_type, Model_Domain
        - [End EMI Model]