

File Header Section

[IBIS Ver]

[Comment Char]

[File Name]

[File Rev]

[Date]

[Source]

[Notes]

[Disclaimer]

[Copyright]

[Component]      Si\_location, Timing\_location

[Manufacturer]

[Package]      R\_pkg, L\_pkg, C\_pkg

[Pin]      signal\_name, model\_name, R\_pin, L\_pin, C\_pin

[Package Model]

[Alternate Package Models]

[End Alternate Package Models]

[Pin Mapping]      pulldown\_ref, pullup\_ref, gnd\_clamp\_ref, power\_clamp\_ref, ext\_ref

[Diff Pin]      inv\_pin, vdiff, tdelay\_typ, tdelay\_min, tdelay\_max

[Series Pin Mapping]      pin\_2, model\_name, function\_table\_group

[Series Switch Groups]      On, Off