

3A-4 Keyword Hierarchy

.ibs FILE

File Header Section

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- [Comment Char]
- [File Name]
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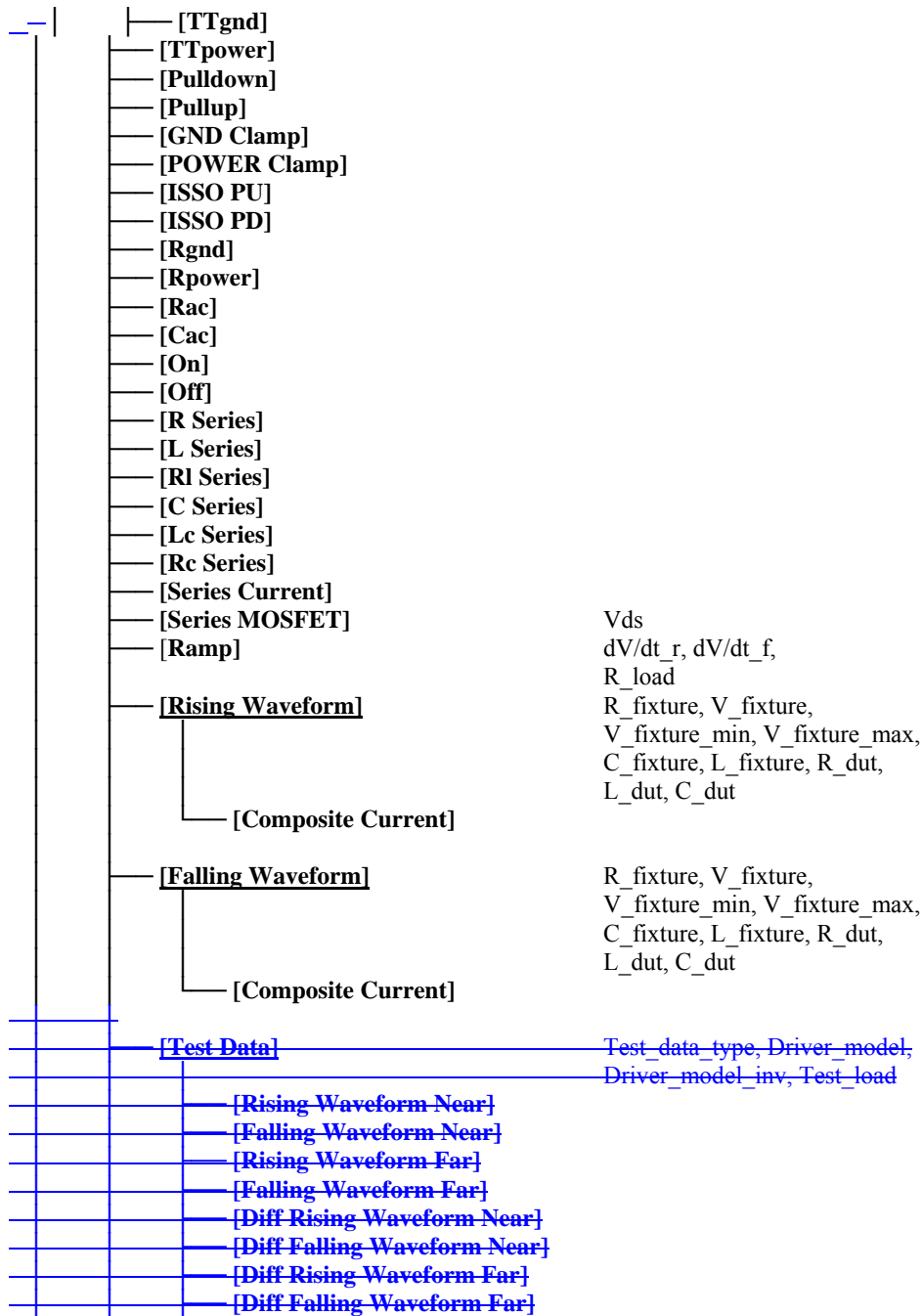
[Component]

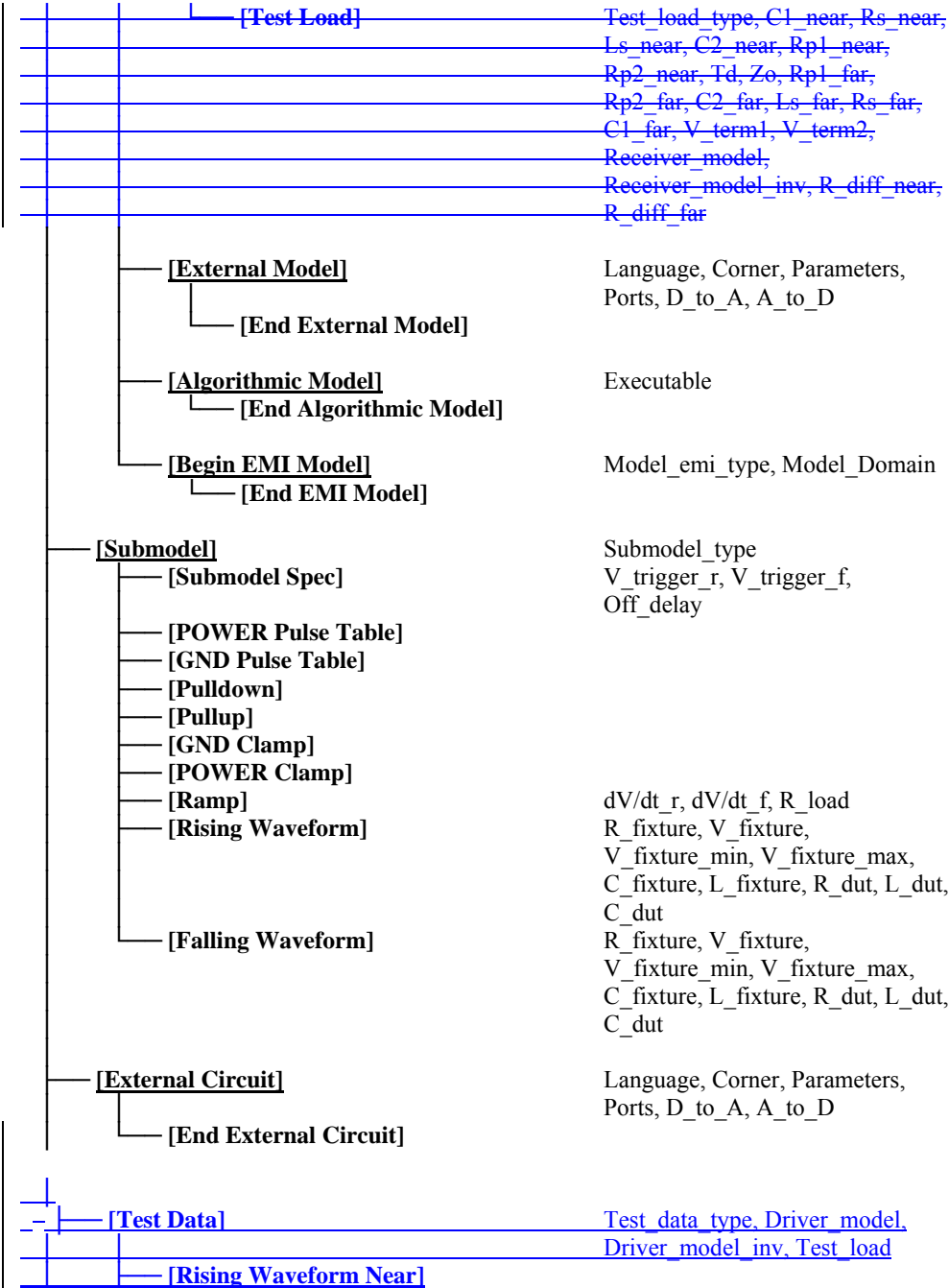
- [Manufacturer] Si_location, Timing_location
- [Package] R_pkg, L_pkg, C_pkg
- [Pin] signal_name, model_name, R_pin, L_pin, C_pin
- [Package Model]
 - [Alternate Package Models]
 - [End Alternate Package Models]
- [Pin Mapping] pulldown_ref, pullup_ref, gnd_clamp_ref, power_clamp_ref, ext_ref
- [Diff Pin] inv_pin, vdiff, tdelay_typ, tdelay_min, tdelay_max
- [Series Pin Mapping] pin_2, model_name, function_table_group
- [Series Switch Groups] On, Off
- [Node Declarations]
 - [End Node Declarations]
- [Circuit Call]
 - [End Circuit Call] Signal_pin, Diff_signal_pins, Series_pins, Port_map
- [Begin EMI Component]
 - [Pin EMI] Domain, Cpd, C_Heatsink_gnd, C_Heatsink_float
 - [Pin Domain EMI] domain_name, clock_div
 - [End EMI Component] percentage

[Model Selector]	
[Model]	Model_type, Polarity, Enable, Vinl, Vinh, C_comp, C_comp_pullup, C_comp_pulldown, C_comp_power_clamp, C_comp_gnd_clamp Vmeas, Cref, Rref, Vref Rref_diff, Cref_diff
[Model Spec]	Vinh, Vinl, Vinh+, Vinh-, Vinl+, Vinl-, S_overshoot_high, S_overshoot_low, D_overshoot_high, D_overshoot_low, D_overshoot_time, D_overshoot_area_h, D_overshoot_area_l, D_overshoot_ampl_h, D_overshoot_ampl_l, Pulse_high, Pulse_low, Pulse_time, Vmeas, Cref, Rref, Vref, Cref_rising, Cref_falling, Rref_rising, Rref_falling, Vref_rising, Vref_falling, Vmeas_rising, Vmeas_falling, Rref_diff, Cref_diff, <u>Weak_R, Weak_I, Weak_V</u>
[Receiver Thresholds]	Vth, Vth_min, Vth_max, Vinh_ac, Vinh_dc, Vinl_ac, Vinl_dc, Threshold_sensitivity, Reference_supply, Vcross_low, Vcross_high, Vdiff_ac, Vdiff_dc, Tslew_ac, Tdiffslew_ac
[Add Submodel]	
[Driver Schedule]	
[Temperature Range]	
[Voltage Range]	
[Pullup Reference]	
[Pulldown Reference]	
[POWER Clamp Reference]	
[GND Clamp Reference]	
[External Reference]	
[C Comp Corner]	<u>C_comp, C_comp_pullup,</u> <u>C_comp_pulldown,</u> <u>C_comp_power_clamp,</u> <u>C_comp_gnd_clamp</u>

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—	[Falling Waveform Near]	
—	[Rising Waveform Far]	
—	[Falling Waveform Far]	
—	[Diff Rising Waveform Near]	
—	[Diff Falling Waveform Near]	
—	[Diff Rising Waveform Far]	
—	[Diff Falling Waveform Far]	
—	[Test Load]	Test load type, C1_near, Rs_near, Ls_near, C2_near, Rp1_near, Rp2_near, Td, Zo, Rp1_far, Rp2_far, C2_far, Ls_far, Rs_far, C1_far, V_term1, V_term2, Receiver_model, Receiver_model_inv, R_diff_near, R_diff_far

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- [Define Package Model]
  - [Manufacturer]
  - [OEM]
  - [Description]
  - [Number Of Sections]
  - [Number Of Pins]
  - [Pin Numbers]
  - [Model Data]
    - [Resistance Matrix]
      - [Bandwidth]
      - [Row]
    - [Inductance Matrix]
      - [Bandwidth]
      - [Row]
    - [Capacitance Matrix]
      - [Bandwidth]
      - [Row]
    - [End Model Data]
  - [End Package Model]
- [End]
  
```

Len, L, R, C, Fork, Endfork

Banded_matrix, Sparse_matrix,
Full_matrix

Banded_matrix, Sparse_matrix,
Full_matrix

Banded_matrix, Sparse_matrix,
Full_matrix

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[Define Package Model]

- [Manufacturer]
- [OEM]
- [Description]
- [Number Of Sections]
- [Number Of Pins]
- [Pin Numbers]
- [Model Data]**

Len, L, R, C, Fork, Endfork

[Resistance Matrix]

Banded_matrix, Sparse_matrix,
Full_matrix

- [Bandwidth]
- [Row]

[Inductance Matrix]

Banded_matrix, Sparse_matrix,
Full_matrix

- [Bandwidth]
- [Row]

[Capacitance Matrix]

Banded_matrix, Sparse_matrix,
Full_matrix

- [Bandwidth]
- [Row]

[End Model Data]

[End Package Model]

[End]

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