

1 Courier 10 and Courier 10 spaced symbol bars

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—	<u>File Header Section</u>
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—	 <u>[Component]</u>
—	[Manufacturer]
—	[Package]
—	[Pin]
—	[Package Model]
—	[Alternate Package Models]
—	[End Alternate Package Models]
—	[Pin Mapping]
—	[Diff Pin]
—	[Series Pin Mapping]
—	[Series Switch Groups]
—	[Node Declarations]
—	[End Node Declarations]
—	[Circuit Call]
—	[End Circuit Call]
—	[Begin EMI Component]
—	[Pin EMI]
—	[Pin Domain EMI]
—	[End EMI Component]
—	 <u>[Model Selector]</u>
—	 <u>[Model]</u>
	Si_location, Timing_location
	R_pkg, L_pkg, C_pkg
	signal_name, model_name, R_pin,
	L_pin, C_pin
	pulldown_ref, pullup_ref,
	gnd_clamp_ref, power_clamp_ref,
	ext_ref
	inv_pin, vdiff, tdelay_typ,
	tdelay_min, tdelay_max
	pin_2, model_name,
	function_table_group
	On, Off
	Signal_pin, Diff_signal_pins,
	Series_pins, Port_map
	Domain, Cpd, C_Heatsink_gnd,
	C_Heatsink_float
	domain_name, clock_div
	percentage
	Model_type, Polarity, Enable,
	Vinl, Vinh, C_comp, C_comp_pullup,
	C_comp_pulldown,
	C_comp_power_clamp,
	C_comp_gnd_clamp
	Vmeas, Cref, Rref, Vref
	Rref_diff, Cref_diff

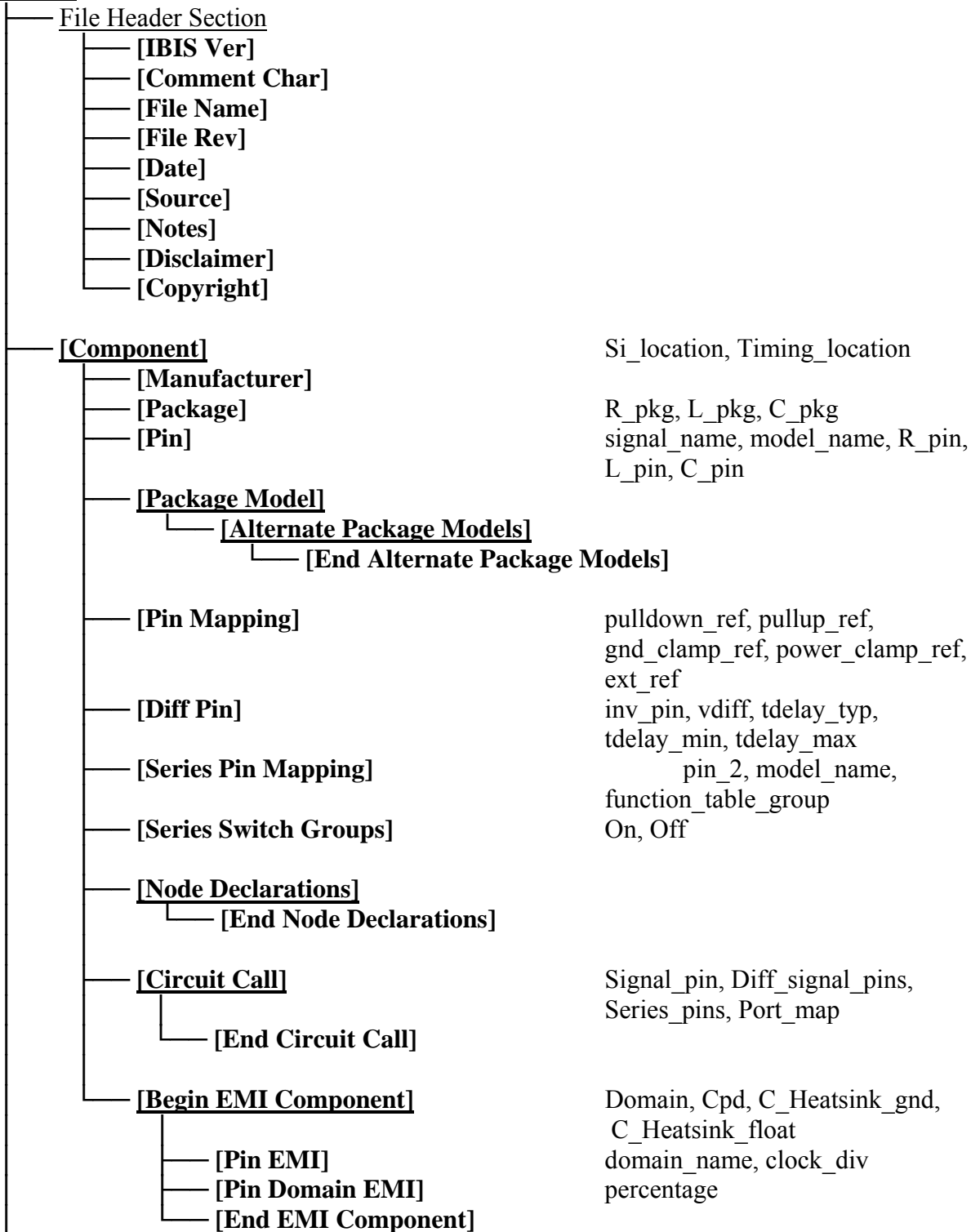
2 Time New Roman 10 and Courier 10 spaced symbol bars

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[Manufacturer]	
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[Pin]	signal_name, model_name, R_pin, L_pin, C_pin
[Package Model]	
[Alternate Package Models]	
[End Alternate Package Models]	
[Pin Mapping]	pullup_ref, pullup_ref, gnd_clamp_ref, power_clamp_ref, ext_ref
[Diff Pin]	inv_pin, vdiff, tdelay_typ, tdelay_min, tdelay_max
[Series Pin Mapping]	pin_2, model_name, function_table_group
[Series Switch Groups]	On, Off
[Node Declarations]	
[End Node Declarations]	
[Circuit Call]	Signal_pin, Diff_signal_pins, Series_pins, Port_map
[End Circuit Call]	
[Begin EMI Component]	Domain, Cpd, C_Heatsink_gnd, C_Heatsink_float
[Pin EMI]	domain_name, clock_div
[Pin Domain EMI]	percentage
[End EMI Component]	
[Model Selector]	
[Model]	Model_type, Polarity, Enable, Vinl, Vinh, C_comp, C_comp_pullup, C_comp_pulldown,

3 Times New Roman 12 and Times New Roman 12 spaced symbol bars

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4 Times New Roman 12 and Courier 10 spaced symbol bars

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└─ [Manufacturer]	
└─ [Package]	R_pkg, L_pkg, C_pkg
└─ [Pin]	signal_name, model_name, R_pin, L_pin, C_pin
└─ <u>[Package Model]</u>	
└─ <u>[Alternate Package Models]</u>	
└─ [End Alternate Package Models]	
└─ [Pin Mapping]	pulldown_ref, pullup_ref, gnd_clamp_ref, power_clamp_ref, ext_ref
└─ [Diff Pin]	inv_pin, vdiff, tdelay_typ, tdelay_min, tdelay_max
└─ [Series Pin Mapping]	pin_2, model_name, function_table_group
└─ [Series Switch Groups]	On, Off
└─ <u>[Node Declarations]</u>	
└─ [End Node Declarations]	
└─ <u>[Circuit Call]</u>	Signal_pin, Diff_signal_pins, Series_pins, Port_map
└─ [End Circuit Call]	
└─ <u>[Begin EMI Component]</u>	Domain, Cpd, C_Heatsink_gnd, C_Heatsink_float
└─ [Pin EMI]	domain_name, clock_div
└─ [Pin Domain EMI]	percentage
└─ [End EMI Component]	