

Minutes of IBIS Qualification Committee Conference Call:  
03/20/02  
11:00am to 12:00pm

IBIS Qualification Committee Members:

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Everyone in attendance marked by \*

Meeting started by everyone introducing themselves and giving a description of why they want to be involved in the IBIS Quality committee. Many common themes, here is a summary

- IBIS Users frustrated by poor quality of IBIS models and want a way to improve
- Lots of engineers from many companies doing the same validation work on the same models. Countless hours wasted.
- Semi-conductor vendors participants want to know what is important to their customers (ie what makes a high quality model)
- Consumers of semiconductor components want a standardized way to spec requirements for IBIS quality at procurement level
- Number of IBIS models required for a company has grown significantly over time. No way for companies to validate all models. Too many models to validate.
- It takes a lot of man power to validate an IBIS model.
- Semiconductor vendors often say that “none of our other customers have any issues with this model” when problems are reported. Users want a standardized way of communicating issues with models.

Covered charter and deliverables of committee

- Brought up the concept of a checklist of items. Discussion followed.
  - Something simple, and easy to use. Don't have to be a modeling engineer to figure out.
  - Needs to be useful for a wide audience
  - Communicate to vendors/users.
  - To cover overall file, component, and model sections
  - Check for completeness, correctness, accuracy
  - Leverage of prior work (ie Accuracy Spec, Siemens Spec, Roy Leventhol document)
- The official charter is to develop a complete checklist and establish levels of quality, as they would pertain to items on the checklist. This checklist should serve to be incorporated directly into procurement specs.

Reviewed some of the items that should be in checklist

- Barry read entries from the sample checklist put together by SiSoft.
- Additional comments from the group followed.
  - Specific component number from vendor that model represents should be in file.
  - Verify values of C-comp
  - Pin parasitics
  - Documentation to backup creation/correlation.
    - What HSPICE model was used to create?
    - Did it correlate in the lab?
    - What correlation was done?
    - Assumptions made.
  - Accuracy levels from IO Buffer Accuracy Handbook included in checklist.
  - Need to support notes for each checklist item
  - Detailed checking of curves
  - Visual inspection
  - Need to feedback to the good vendors that we like what they're doing.
  - Don't want each consumer to have a different procurement spec. Need a standard. (A good thing for our charter/deliverable)
  - Methodology and tools for meeting the checklist to be provided by EDA industry

Discussion of how often we should meet?

- Bi-weekly for one hour decided
- Everyone to send Barry their schedule and he will try to set up a time for a normal meeting.

Discussion on whether to make this a standard?

- We don't know yet.
- Wait and see.
- More important to have a common voice with a common document.

Action items:

- Everyone send availability for 11:00EST bi-weekly meeting
- Everyone to send Barry references to prior-work that was not referenced
- Everyone to send items for checklist that they think should be included
- Mike to set up mailing list
- SiSoft to setup website to store documentation
- Barry to recruit volunteer for phone bridge
- Barry, Eric, Kevin to compile feedback that comes in over the next week into checklist for review at next meeting
- Barry to email accuracy checklist for reference  
<http://www.eda.org/pub/ibis/accuracy/checklist.txt>
- Barry to determine meeting date and send invite (Planning for week of 4/1)

