**IBIS Summit at 2021 IEEE Virtual Symposium on EMC+SIPI**

August 12, 2021

**Agenda**

All times PDT

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| 8:00 AM | **CHECK IN, INTRODUCTION TO MEETING**  Randy Wolff (Micron Technology, USA)  (Chair, IBIS Open Forum) |
| 8:10 AM | **IBIS Chair’s Report**  Randy Wolff (Micron Technology, USA)  (Chair, IBIS Open Forum) |
| 8:25 AM | **Checking and Converting Touchstone Files with TSCHK2**  Mike LaBonte (The MathWorks, USA) |
| 8:45 AM | **Expectations for IBIS 7.1**  Michael Mirmak (Intel Corp., USA) |
| 9:00 AM | **Analysis to Measurement Validation with S-Parameters Similarity Metric**  Yuriy Shlepnev (Simberian, USA) |
| 9:35 AM | **New Way to Improve Power Supply Induced Jitter Simulation Accuracy for IBIS Model**  Yifan Ding\*, Yin Sun\*\*, Zhiping Yang\*\*\*, Chulsoon Hwang\*  (\*Missouri S&T, USA; \*\*Zhejiang University, China; \*\*\*Google (Waymo), USA)  [Presented by Yifan Ding (Missouri S&T, USA)] |
| 10:05 AM | **BREAK (10 minutes)** |
| 10:15 AM | **Fast PDN Impedance Prediction Using Deep Learning**  Ling Zhang\*, Jack Juang\*, Zurab Kiguradze\*, Bo Pu\*, Shuai Jin\*\*  Songping Wu\*\*, Zhiping Yang\*\*, Jun Fan\*, Chulsoon Hwang\*  (\*Missouri S&T, USA; \*\*Google, USA)  [Presented by video recording, Ling Zhang (Missouri S&T, USA)] |
| 10:40 AM | **GDDR6X IBIS Modeling**  Randy Wolff\*, Arpad Muranyi\*\*  (\*Micron Technology, USA; \*\*Siemens EDA, USA)  [Presented by Randy Wolff\*, Arpad Muranyi\*\*  (\*Micron Technology, USA; \*\*Siemens EDA, USA)] |
| 11:10 AM | **Next Generation IBIS-AMI Modeling**  Walter Katz (The MathWorks, USA) |
| 11:45 AM | **OPEN DISCUSSION AND CONCLUDING ITEMS**  - Next IBIS Summit with DesignCon 2021 on August 19, 2021  - Next IBIS Open Forum Meeting on August 27, 2021 |
| 12:00 PM | **END OF MEETING** |

