IBIS-ATM Task Group Report

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Progress since DesignCon 2020 (Jan. 31 2020)
(my last ATM progress report)

- **IBIS v7.1 was released on December 10, 2021**
  - **This specification includes 15 BIRDs**

- **BIRDs <u>approved</u> since IBIS v7.1 was released**
  - **211.4   IBIS AMI Reference Flow Improvements**
  - **213.1   Extending IBIS-AMI for PAMn Analysis**
  - **217   Require Clocked Rx Models to Return Clock Times**
  - **218   Designator Pin List Relaxation**

- **BIRDs <u>rejected</u> since IBIS v7.1 was released**
  - **166.4   Resolving problems with Redriver Init Flow**
  - **181.1   I-V Table Clarifications**
  - **190   Clarification for Redriver Flow**
  - **210   New Redriver AMI Flow**

Pending BIRDs, BIRD drafts, discussion topics

- **219.1**   **AMI Parameter Root Name Clarifications**

- **Standard Power Integrity Model (SPIM) in IBIS**
- **Power Supply Induced Jitter (PSIJ) study**
- **VRM, diode and inductor modeling**
- **Multi-level analog buffer modeling (PAMn, C-phy, etc.)**

Questions / comments?