ASIAN IBIS Summit Dec 2005
Slideshow

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Contributors

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The setup..
ASIAN IBIS SUMMIT
---亚洲IBIS技术研讨会
会议地点：威尼斯二号宴会厅（一楼）
ASIAN IBIS SUMMIT

December 6, 2005
Crowne Plaza Hotel
Shenzhen, CHINA

2005年12月6日
威尼斯酒店
中国 深圳

HUAWEI TECHNOLOGIES

ELA IBIS OPEN FORUM
http://www.eigroup.org/ibis/
Attendees..
Presentors..
ASIAN IBIS SUMMIT

December 5, 2005
Shenzhen, P. R. China

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IBIS Models have played an important role in signal integrity analysis.

- Access Buffer Characteristics
- Waveform Quality Check
- Timing Analysis
Loose Timing Budget System Analysis

- Definition
- Required inputs
  - Timing topology
  - Component-level timing
  - Operating clock frequency
  - Approximated flight time
What are the Objectives of the SI Engineer?

- Maximize performance
  Highest possible clock speed
- Ensure reliable operation
  Meet timing constraints
  Keep overshoot below levels that will cause device malfunction or destruction.
  Maintain adequate switching margins
- Minimize per unit costs
  Minimize the number of components (including terminators)
  Use lowest cost PCB materials
JEITA EDA -WG Activity and Study of Interconnect Model

Dec 6, 2003
Asian IBIS SUMMIT in Thailand

JEITA EDAN
Takeshi Watanabe (PM)

JEITA : Japan Electronics and Information Technology Industry Association
Lunch with the Press..
IBIS and Power Delivery Systems

Jiang, Xiangzhong
Li, Jinjun
Zhang, Shengli

Huawei Technologies
Power Delivery System, Signal Return Path, and Simultaneous Switching Output Analysis Guidelines

Asia IBIS Summit

Raymond Y. Cheng
Sam Chitwood
Sigrity, Inc.
Using IBIS for SI Analysis
Modeling methodology

- Modeling is quite involved, it covers active devices as well as passive devices, such as package, transmission line, connector, via, and plane etc.
- Not all modeling methods are the same. They have tradeoffs and are suitable for different applications.
- There are behavioral IBIS and structural Spice modeling for active devices. Spice model is applicable in design and demanding situations, while IBIS model is used in system and board level simulation.
- Circuit simulators can run both IBIS and Spice models. They have different characteristics.
Practical Measurement vs. Simulation Correlation with DDR2 667 Interface

Kazuyoshi Shoji
Hitachi ULSI Systems
Improving IBIS ECL Algorithms

Bob Ross
Asian IBIS Summit
Shenzhen, CHINA
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Breaks and Demos..
The End