AGENDA - IBIS SUMMIT MEETING AT DESIGNCON 2010

Thursday, February 4, 2010 Santa Clara Convention Center Santa Clara, California Room: 203 Sponsors: Cisco Systems, DesignCon, IBIS Open Forum

- 8:00 AM Refreshments & Sign In
- 8:30 AM Official Opening
 - Welcome to Summit
 - Introductions
 - Opens for Issues & Discussion Topics
- 8:45 AM IBIS Activities and Future Plans Bob Ross, Teraspeed Consulting Group
- 9:05 AM Top 10 Issues As Seen During IBIS Model Reviews Lynne Green, Green Streak Programs
- 9:25 AM IBIS Quality Checklist Rev. 2.0 Moshiul Haque and Randy Wolff, Micron Technology
- 9:55 AM Pin-Pair Oriented Extraction Method for Differential Pair IBIS Modeling Lance Wang, IO Methodology
- 10:15 AM Break
- **10:30 AM** Signal Loop Inductance in [Pin] and [Package Model] Randy Wolff, Micron Technology
- **11:00 AM An Introduction to Model Connection Protocols** Brad Brim, Sigrity
- **11:20 AM Quality Metrics for S-parameter Models** Yuriy Shlepnev, Simberian
- 12:00 PM Lunch (Hosted by Cisco Systems and IBIS Open Forum)

AGENDA - IBIS SUMMIT MEETING (Continued)

12:00 PM Lunch - (Hosted by Cisco Systems and IBIS Open Forum)

- **1:00 PM SI/PI Co-Analysis and Linearity Indicator** Myoung Joon Choi and Vishram Pandit, Intel Corporation
- 1:30 PM IBIS-ATM Task Group Report Arpad Muranyi, Mentor Graphics Corporation
- 1:40 PM Predicting BER with IBIS-AMI: Experiences Correlating SerDes Simulations and Measurement Todd Westerhoff*, Adge Hawes**, Michael Steinberger*, Kent Dramstad**, Walter Katz*, Barry Katz*, *Signal Integrity Software (SiSoft), and **IBM
- 2:20 PM Conditional Expressions in IBIS-AMI Adge Hawes, IBM
- 2:40 PM Growing Pains with IBIS-AMI Modeling Arpad Muranyi, Mentor Graphics Corporation
- 3:05PM Break
- 3:20 PM Open Discussion Future Projects
- 4:45 PM Concluding Items
 - Next Open Forum Meeting: February 19, 2010
 European IBIS Summit: May 12, 2010, Hildesheim, Germany with Signal Propogation on Interconnects Workshop
- 5:00 PM End of Meeting Room Availability