

# **IBIS-ATM task group report**

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# IBIS-ATM Task Group Report

- **The IBIS Interconnect SPICE Subcircuit (IBIS-ISS) specification draft was last updated on August 11, 2009**  
[http://eda.org/pub/ibis/macromodel\\_wip/archive/20090811/walterkatz/IBIS-ISS%20Specification%20draft/IBIS\\_ISS\\_DRAFTo2.pdf](http://eda.org/pub/ibis/macromodel_wip/archive/20090811/walterkatz/IBIS-ISS%20Specification%20draft/IBIS_ISS_DRAFTo2.pdf)
  - at the time it was considered to be finished and was supposed to be submitted to the IBIS Open Forum
  - however, several questions were left unanswered which need to be addressed before it can be handed over to the IBIS Open Forum
- **Since August 2009, the ATM group has been busy on finding corrections to the problems discovered in the AMI specification**
  - Tx GetWave digital input
  - Init just returns buffer equalization
  - correcting the mistake in the existing reference flow and make some improvements  
[http://eda.org/pub/ibis/macromodel\\_wip/archive/20091118/arpadmuranyi/Final%20AMI%20flow%20for%20IBIS%205.1/AMI\\_Flows\\_5\\_final.pdf](http://eda.org/pub/ibis/macromodel_wip/archive/20091118/arpadmuranyi/Final%20AMI%20flow%20for%20IBIS%205.1/AMI_Flows_5_final.pdf)
  - drafting a BIRD document with corrections and improvements
  - a proposal was also presented with addressing analog non-LTI buffer behaviors  
[http://eda.org/pub/ibis/macromodel\\_wip/archive/20091014/vladimirdmitrievzdorov/How%20to%20account%20for%20non-LTI%20of%20Tx%20analog%20buffer%20in%20IBIS%20AMI%20flow/AMI\\_Tx\\_With\\_Admittance.pdf](http://eda.org/pub/ibis/macromodel_wip/archive/20091014/vladimirdmitrievzdorov/How%20to%20account%20for%20non-LTI%20of%20Tx%20analog%20buffer%20in%20IBIS%20AMI%20flow/AMI_Tx_With_Admittance.pdf)
- **The ATM group is currently neck deep in editing the BIRD draft**
  - goal is to finish editing and discussing the BIRD by the end of Q1 2010 and submit it to the IBIS Open Forum for consideration
- **After completion of the AMI BIRD, we will return to the IBIS-ISS specification and the EBD/EMD/Electrical Model Connectivity proposals**