# IBIS-ATM Task Group Report

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# Progress since DesignCon 2018

- **There was no new IBIS specification release in 2018**

- **BIRDs <u>approved</u> since DesignCon 2018:**
  - **189.7    Interconnect Modeling Using IBIS-ISS and Touchstone**
  - **193        Figure 29 corrections**
  - **194        Revised AMI Ts4file Analog Buffer Models**
  - **195.1    Enabling [Rgnd] and [Rpower] Keywords for Input Models**
  - **196.1    Prohibit Periods at the End of File Names**

- **BIRDs <u>rejected</u> since DesignCon 2018:**
  - **125.1    Make IBIS-ISS Available for IBIS Package Modeling (7 years 15 days)**
  - **145.3    Cascading IBIS I/O buffers with [External Circuit]s using the [Model Call] keyword**
  - **163        Instantiating and Connecting [External Circuit] Package Models with [Circuit Call]**
  - **164        Allowing Package Models to be defined in [External Circuit]**

# Pending BIRDs and BIRD drafts awaiting discussion / vote

- **166.4**     **Resolving problems with Redriver Init Flow**
- **181.1**     **I-V Table Clarifications**
- **190**     **Clarification for Redriver Flow**
- **197.2**     **New AMI Reserved Parameter DC_Offset**

- **TBD**     **New proposal for Redriver Flow (may replace BIRD166)**
- **TBD**     **C_comp modeling with IBIS-ISS**

**Discussions on the Redriver BIRDs are in need of feedback from the industry as there are multiple options to move forward and we would like to implement what would be the best for all.**

**We are also discussing various topics on how the AMI specification might need to be changed to support single ended (DDR5) modeling and simulations**

# Questions / comments?