



Bridging the Gap: Channel Operating Margin (COM) and IBIS-AMI for Analysis and Development of High-Speed SerDes Designs

Graham Kus, Signal Edge Solutions, LLC

**DesignCon Hybrid IBIS Summit
Santa Clara, CA
February 27, 2026**



SPEAKERS



Graham J. Kus

Principal Signal and Power Integrity Engineer, Signal Edge Solutions, LLC

graham@signaledgesolutions.com | www.signaledgesolutions.com.com | @si_pi_expert

Graham Kus has over a decade of experience with Signal and Power Integrity workflows along with a background in Digital Signal Processing and Communications Systems engineering. His Industry and EDA efforts have included enterprise datacenter products with semi-custom ASICs, PHY and system reference designs, as well as IBIS-AMI model development for various SerDes and JEDEC DDR standards. He is a regular participant of engineering conferences including the DesignCon technical program and IEEE EMC+SIPI symposium. Graham is a member of IEEE, the ACM, is currently serving as Secretary on the IBIS Open Forum committee and holds a Bachelor of Science in Electrical and Computer Engineering from Northeastern University in Boston, Massachusetts.



Bridging the Gap Between COM and IBIS-AMI

Abstract:

This presentation discusses the advantages and disadvantages for two types of channel analysis applicable to hardware design. The channel links involve close-margin signal to noise such as with 112 or 224G PAM-4 per lane. The comparison is made between using Channel Operating Margin (COM) and IBIS-AMI (specifically Time Domain simulation). COM is first defined by IEEE 802.3bj-2014. The IBIS-AMI specification is defined by the IBIS Committee.

Key points of this presentation:

- In DOE efforts during system design, COM analysis has advantages over IBIS-AMI model simulations.
- Comparison is shown between COM and IBIS-AMI regarding compute execution performance.
- System design can be accelerated by combining COM-centric processes with IBIS-AMI capabilities.
- Proposal for IBIS to provide- and enforce- existing IBIS IQ Quality specification as a compliance or logo certification.



“Establish a Channel for Traffic”



[Agpigeon, CC0, via Wikimedia Commons](#)

- Ferry Line Solution:
- Not multiple lanes
- A “Packet” of vehicles
- A vessel: is always sinking

VS.



[Estormiz, CC0, via Wikimedia Commons](#)

- Causeway Solution:
- 1 Lane throughput
- Still a “packet” of vehicles
- Blocks naval traffic

These are ways to move traffic from point A to point B across an obstacle like water.

These are like communication channels to transport vehicle traffic.



“Next: Increase Channel Capacity”



© Frank Schulenburg, via Wikimedia Commons

- Twin Tower Suspension Bridge:
- Easy to build – but lots of material
- Moves multiple lanes
- Supports land and naval traffic

VS.



Podstawko, CC BY-SA 4.0, via Wikimedia Commons

- Self-Anchored Suspension Bridge:
- Difficult to build - but half the material
- Moves multiple lanes
- Supports land and naval traffic

These bridge designs are ways to think how communications channels can be designed.

Some can be good-performance and inexpensive.

Others can be high-performance and very expensive.



Bridging the Gap Between COM and IBIS-AMI

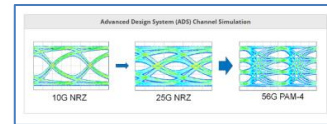
■ COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- Standard or Custom?
- Availability?
- Transferrable results B2B?
- Usefulness?

■ IBIS-AMI Pros + Cons:

- Standard or Custom?
- Availability?
- Transferrable results B2B?
- Usefulness?



COM equation: Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

IBIS-AMI waveforms for 10G, 25G NRZ and 56G PAM-4, PAM-4 Simulation and Design of Next Generation High-Speed Digital Links, www.keysight.com, 2026.



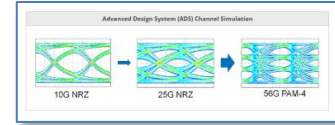
Bridging the Gap Between COM and IBIS-AMI

■ COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- Standard: Each specific standard details COM
 - ✓ Documented: *IEEE, OIF, PCI-SIG, USB, etc.*
 - ✓ Open-source: *defined with equations*
- Availability:
- Transferrable results B2B:
- Usefulness:

■ IBIS-AMI Pros + Cons:



- Custom: Model is from Silicon Vendor or EDA-Library
 - ❑ Documented: *Is it? Where? By whom?*
 - ❑ Proprietary: *compiled and obfuscated code*
- Availability:
- Transferrable results B2B:
- Usefulness:

COM equation: Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

IBIS-AMI waveforms for 10G, 25G NRZ and 56G PAM-4, PAM-4 Simulation and Design of Next Generation High-Speed Digital Links, www.keysight.com, 2026.



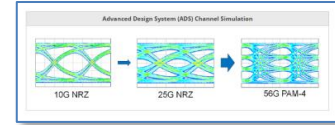
Bridging the Gap Between COM and IBIS-AMI

■ COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- Standard: Each specific standard details COM
 - ✓ Documented: IEEE, OIF, PCI-SIG, USB, etc.
 - ✓ Open-source: defined with equations
- Availability:
 - ✓ Public reference code in 2+ languages
 - ✓ MATLAB® and Python®
- Transferrable results B2B:
- Usefulness:

■ IBIS-AMI Pros + Cons:



- Custom: Model is from Silicon Vendor or EDA-Library
 - ❑ Documented: Is it? Where? By whom?
 - ❑ Proprietary: compiled and obfuscated code
- Availability:
 - ❑ Arbitrary with Vendor or EDA tool library
- Transferrable results B2B:
- Usefulness:

COM equation: Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

IBIS-AMI waveforms for 10G, 25G NRZ and 56G PAM-4, PAM-4 Simulation and Design of Next Generation High-Speed Digital Links, www.keysight.com, 2026.



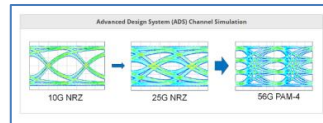
Bridging the Gap Between COM and IBIS-AMI

■ COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- Standard: Each specific standard details COM
 - ✓ Documented: *IEEE, OIF, PCI-SIG, USB, etc.*
 - ✓ Open-source: *defined with equations*
- Availability:
 - ✓ Public reference code in 2+ languages
- Transferrable results B2B:
 - ✓ Yes, without NDA
- Usefulness:

■ IBIS-AMI Pros + Cons:



- Custom: Model is from Silicon Vendor or EDA-Library
 - ❑ Documented: *Is it? Where? By whom?*
 - ❑ Proprietary: *compiled and obfuscated code*
- Availability:
 - ❑ Arbitrary with Vendor or EDA tool library
- Transferrable results B2B:
 - ❑ Usually requires “n-way” NDA
- Usefulness:

COM equation: Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

IBIS-AMI waveforms for 10G, 25G NRZ and 56G PAM-4, PAM-4 Simulation and Design of Next Generation High-Speed Digital Links, www.keysight.com, 2026.



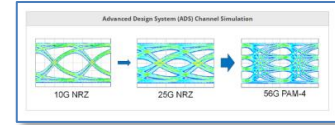
Bridging the Gap Between COM and IBIS-AMI

■ COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- Standard: Each specific standard details COM
 - ✓ Documented: *IEEE, OIF, PCI-SIG, USB, etc.*
 - ✓ Open-source: *defined with equations*
- Availability:
 - ✓ Public reference code in 2+ languages
- Transferrable results B2B:
 - ✓ Yes, without NDA
- Usefulness:
 - ✓ Bugs possible- but peer reviewed code is public
 - ✓ Provides channel margin metric
 - ✓ Provides equalization solution for Tx and Rx
 - ❖ Fast EQ sweep but no waveforms for correlation
 - ✓ AI algorithms can utilize reference code
 - ✓ MLSE available as draft in current IEEE COM

■ IBIS-AMI Pros + Cons:



- Custom: Model is from Silicon Vendor or EDA-Library
 - ❑ Documented: *Is it? Where? By whom?*
 - ❑ Proprietary: *compiled and obfuscated code*
- Availability:
 - ❑ Arbitrary with Vendor or EDA tool library
- Transferrable results B2B:
 - ❑ Usually requires “n-way” NDA
- Usefulness:
 - ❑ Bugs in “black box” without IBIS IQ quality report
 - ❑ Provides channel margin metric
 - ❖ Provides equalization solution for Tx and Rx
 - ❖ Slow EQ sweep with waveforms for correlation
 - ❖ AI algorithms at mercy of EDA-specific host tool
 - ❖ MLSE available with proprietary EDA solutions



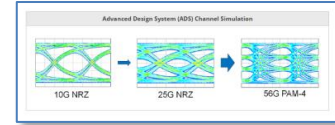
Bridging the Gap Between COM and IBIS-AMI

■ COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- Standard: Each specific standard details COM
 - ✓ Documented: *IEEE, OIF, PCI-SIG, USB, etc.*
 - ✓ Open-source: *defined with equations*
- Availability:
 - ✓ Public reference code in 2+ languages
- Transferrable results B2B:
 - ✓ Yes, without NDA
- Usefulness:
 - ✓ Bugs possible- but peer reviewed code is public

■ IBIS-AMI Pros + Cons:



- Custom: Model is from Silicon Vendor or EDA-Library
 - ❑ Documented: *Is it? Where? By whom?*
 - ❑ Proprietary: *compiled and obfuscated code*
- Availability:
 - ❑ Arbitrary with Vendor or EDA tool library
- Transferrable results B2B:
 - ❑ Usually requires NDA
- Usefulness:
 - ❑ Bugs in "black box" without IBIS IQ quality report

IBIS Proposal:
Require Logo Certification
for IBIS Quality Levels.



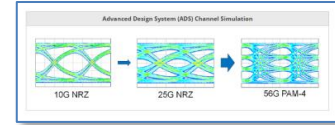
Bridging the Gap Between COM and IBIS-AMI

COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- Standard: Each specific standard details COM
 - ✓ Documented: IEEE, OIF, PCI
 - ✓ Open-source: defined with eq
- Availability:
 - ✓ Public reference code in 2+ la
- Transferrable results B2B:
 - ✓ Yes, without NDA
- Usefulness:
 - ✓ Bugs possible- but peer reviewed code is public
 - ✓ Provides channel margin metric
 - ✓ Provides equalization solution for Tx and Rx
 - ❖ Fast EQ sweep but no waveforms for correlation
 - ✓ AI algorithms can utilize reference code
 - ✓ MLSE available as draft in current IEEE COM

IBIS-AMI Pros + Cons:



- Custom: Model is from Silicon Vendor or EDA-Library
 - Where? By whom?
 - Compiled and obfuscated code
 - Vendor or EDA tool library
- B2B:
 - As NDA
- Usefulness:
 - ❑ Bugs in "black box" without IBIS IQ quality report
 - ❑ Provides channel margin metric
 - ❖ Provides equalization solution for Tx and Rx
 - ❖ Slow EQ sweep with waveforms for correlation
 - ❖ AI algorithms at mercy of EDA-specific host tool
 - ❖ MLSE available with proprietary EDA solutions

Hybrid Workflow:
Use COM to accelerate
IBIS-AMI simulation



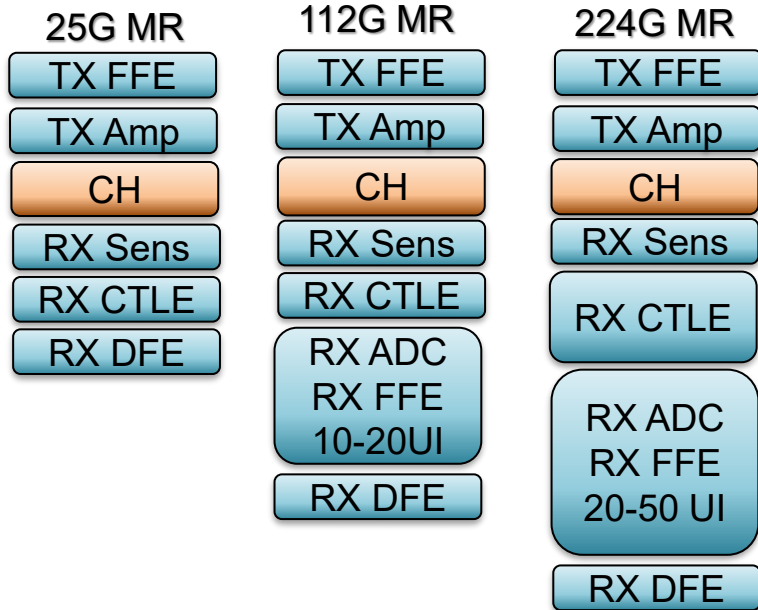
❖ Fast EQ sweep but no waveforms for correlation

❖ Slow EQ sweep with waveforms for correlation

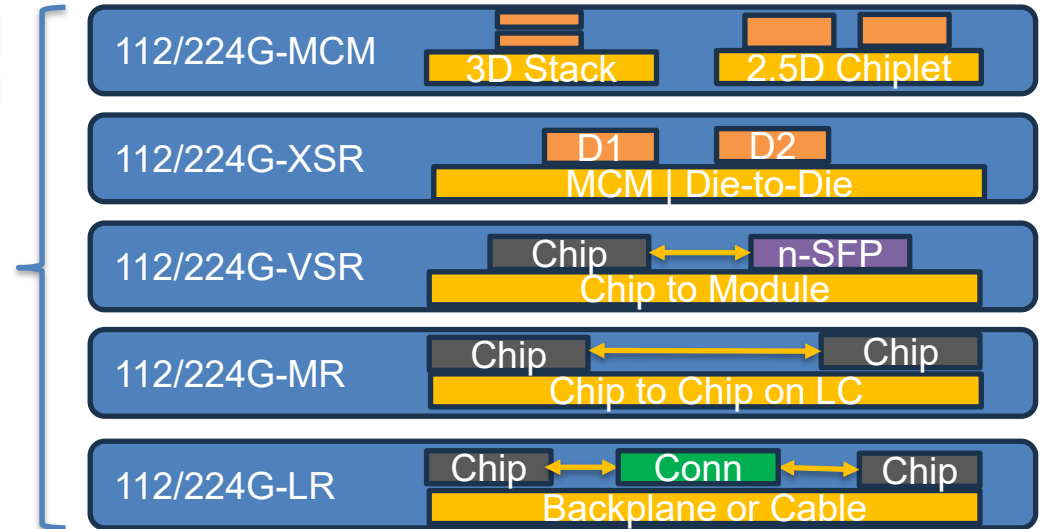


Sidebar: Equalizer Stacks

Generic Complexity Comparison for TX/RX:



Goal: Support these channel classes:



- Complete equalizer stack supports all channel classes
- Power/Cost/Size may exceed needs for channel class in system design



Bridging the Gap Between COM and IBIS-AMI

- COM Pros + Cons:

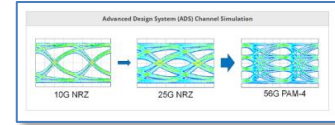
$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- ✓ **FAST:**

1. Equalizer steps compound and cost CPU cycles:
2. Tx FFE{range}*CTLE{range}*CTLE 2{range}...
3. Blind sweeps cost 100k+ simulation cycles
4. Key Advantages:
 - COM allows “envelope” EQ vs. channel loss
 - COM is all convolution on a pulse response
 - COM has no GetWave() function

- ✓ **Cost is minutes.**

- IBIS-AMI Pros + Cons:



- ❖ **SLOW:** Provides equalization solution for Tx and Rx

COM equation: Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

IBIS-AMI waveforms for 10G, 25G NRZ and 56G PAM-4, PAM-4 Simulation and Design of Next Generation High-Speed Digital Links, www.keysight.com, 2026.



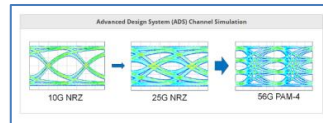
Bridging the Gap Between COM and IBIS-AMI

- COM Pros + Cons:

$$COM = 20 \cdot \log_{10} \left(\frac{A_S}{A_{ni}} \right)$$

- ✓ FAST: Provides equalization solution for Tx and Rx

- IBIS-AMI Pros + Cons:



- ❖ SLOW: Provides equalization solution for Tx and Rx

1. Equalizer steps compound and cost more CPU cycles:
2. Tx FFE{range}*CTLE{range}*CTLE 2{range}...
3. Blind sweeps cost 100k+ simulation cycles
4. Each sweep requires GetWave() if enabled- and it is for:
 1. Finding DFE trained taps
 2. Calculate TD waveform with protocol-encoding

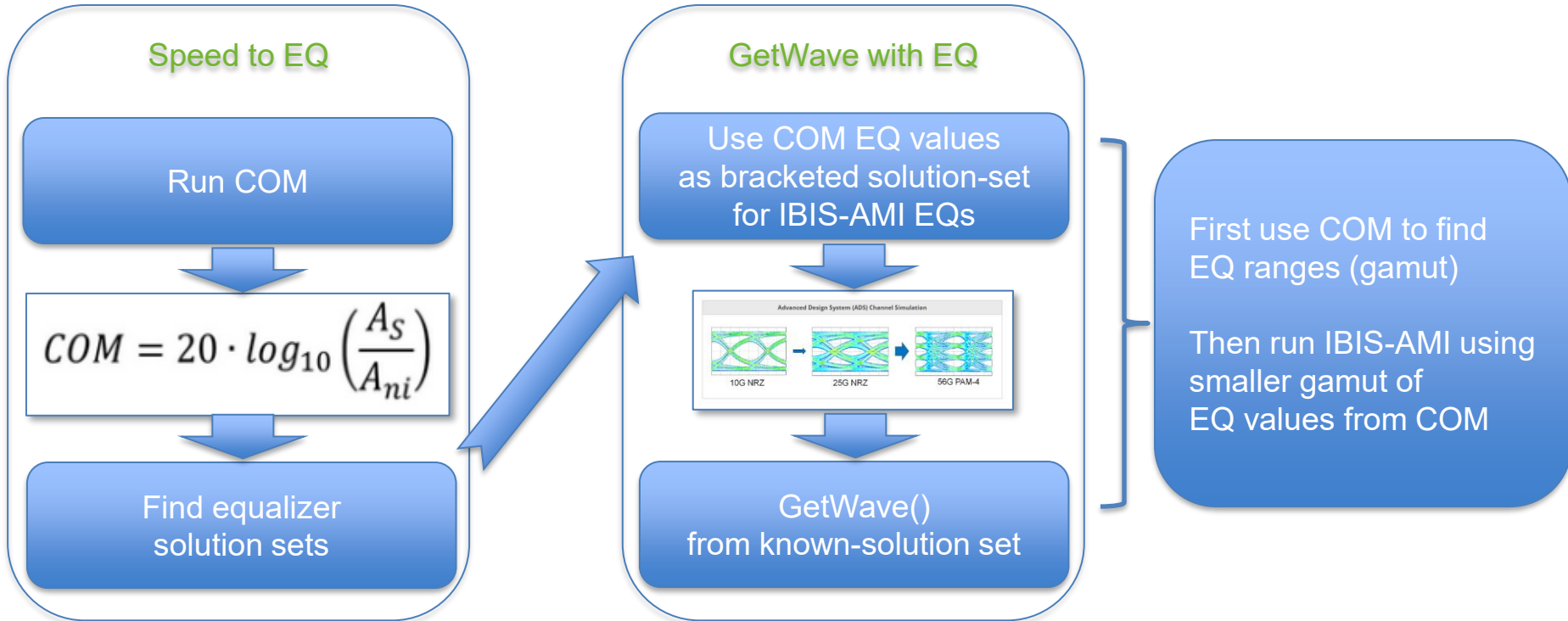
- ❖ Cost is potentially hours per channel class

COM equation: Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

IBIS-AMI waveforms for 10G, 25G NRZ and 56G PAM-4, PAM-4 Simulation and Design of Next Generation High-Speed Digital Links, www.keysight.com, 2026.



Bridging the Gap Between COM and IBIS-AMI



COM equation: Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

IBIS-AMI waveforms for 10G, 25G NRZ and 56G PAM-4, PAM-4 Simulation and Design of Next Generation High-Speed Digital Links, www.keysight.com, 2026.



Bridging the Gap Between COM and IBIS-AMI

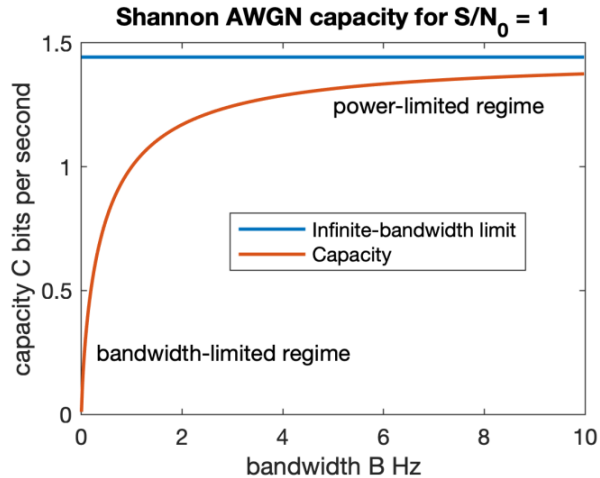
- A tutorial on Channel Operating Margin:



Channel Capacity vs. Reliable Communication

Getting Started:

- Comms Theory- channel capacity definition:



Channel Capacity vs. Bandwidth

Theoretical Channel Capacity

*Isheden at English Wikipedia, GFDL
<<http://www.gnu.org/copyleft/fdl.html>>, via Wikimedia Commons*

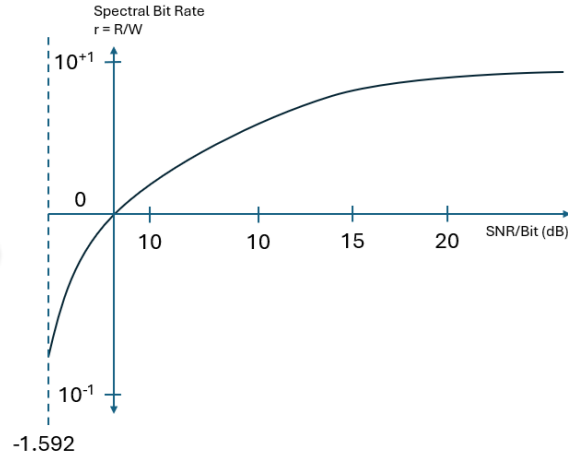


Channel Capacity vs. Reliable Communication

(Still) Getting Started:

- Maximum bit rate governed by SNR per bit:

Maximum rate for
reliable communication

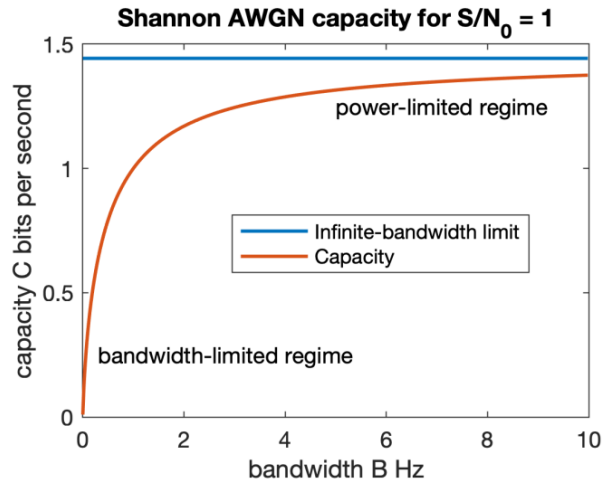


Spectral Bit Rate vs. SNR per bit:
Minimum SNR is theoretical limit

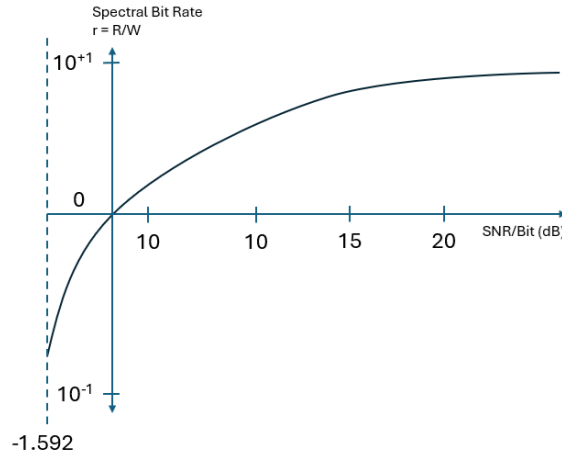
Sec 10.3, *Bounds on Communication*, *Communications Systems Engineering*,
Proakis and Salehi, 1994.



Channel Capacity vs. Reliable Communication



Channel Capacity vs. Bandwidth



Spectral Bit Rate vs. SNR per bit:
Minimum SNR is theoretical limit

Some Mathematics:

Channel Capacity:

Spectral Bit Rate $r = R/W$

R = transmission rate

W = available bandwidth

$r \ll 1$: W is large

$r \gg 1$: W is limited

$$r = \log\left(1 + r \frac{E_b}{N_0}\right)$$

$$\frac{E_b}{N_0} = -1.592 \text{ dB}$$

$$\frac{E_b}{N_0} > 0.693 = SNR_{min}$$

Isheden at English Wikipedia, GFDL
<<http://www.gnu.org/copyleft/fdl.html>>, via Wikimedia Commons

Sec 10.3, Bounds on Communication, Communications Systems Engineering,
Proakis and Salehi, 1994.



Bridging the Gap Between COM and IBIS-AMI

$$COM = 20 \cdot \log_{10} \left(\frac{A_s}{A_{ni}} \right) \quad (93A-1) \quad \left. \vphantom{COM} \right\} COM \geq 3 \text{ dB}$$

COM Table 93A-1 complexity grows with each subsequent Standard for Ethernet:

- 802.3bj (25G NRZ per lane) - introduction of Channel Operating Margin:
 - COM Table 93A-1
 - COM calculation (93A-1)
 - FOM calculation (93A-36) including determination of variable equalizer parameters.
- 802.3ck (112G PAM4 per lane) – increases EQ ranges, adds DFE to COM table:
 - FOM includes additional equalizer parameters,
 - addition of DFE determination of floating tap locations and magnitudes (93A.1.6.1)
- 802.3dj (224G PAM4 per lane) – changes architecture of equalization:
 - introduction of ADC-based equalizer and MSLE for receiver.

COM is basically an SNR and relates to:

- ✓ Channel Capacity
- ✓ Max reliable data rate

Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3ck-2022, IEEE-SA Standards Board, 2022.



Bridging the Gap Between COM and IBIS-AMI

$$FOM = 10 \cdot \log_{10} \left(\frac{A_s^2}{\sigma_{TX}^2 + \sigma_{ISI}^2 + \sigma_J^2 + \sigma_{XT}^2 + \sigma_N^2} \right) \quad (93A-36)$$

Figure of Merit (FOM): how and where Equalization is incorporated into COM:

Determination of variable equalizer parameters (93A.1.6):

“FOM is calculated for each permitted combination [EQ] values per Table 93A–1.

The combination of values that maximizes the FOM [...] is used for the calculation of:

- a) *The interference and noise amplitude in equation 93A.1.7,*
- b) *and the calculation of COM in equation 93A.1.”*

Determination of DFE floating tap locations and magnitudes (93A.1.6.1):

- c) *Find the locations of the floating banks of taps*
- d) *then compute the coefficient magnitudes for all floating taps*

FOM is also an SNR
and relates to:

- ✓ EQ values
- ✓ IF and noise terms
- ✓ Part of COM calculation

Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3bj-2014, IEEE-SA Standards Board, 2014.

Annex 93A.1, Channel Operating Margin, IEEE Standard for Ethernet, IEEE Std 802.3ck-2022, IEEE-SA Standards Board, 2022.



Bridging the Gap Between COM and IBIS-AMI

Adoption of COM by standards organizations:

- 2014: IEEE introduces Channel Operating Margin (COM) in 2014 with 802.3bj Standard for Ethernet
- 2017: OIF-CEI 5.0 adopts COM as part of 56G and higher throughputs
- 2019: PCI-SIG adopts COM for PCIe 6.0
- 2020: USB-IF adopts eCOM for USB4 v2
- Recommend engineering professionals to pay attention to upcoming standards!



Bridging the Gap Between COM and IBIS-AMI

- How can the IBIS Open Forum help?
 - Quality:
 - *IBIS Quality currently provided as a specification*
 - *IBIS Quality is not enforced*
 - *IBIS Quality can be enforced*
 - Documentation: Model from Vendor or EDA-Library
 - *Model documentation is not part of IBIS IQ*
 - *IBIS Quality can also enforce documentation*
 - Solution:
 - *IBIS Logo-testing issued to model vendors*
 - *Potential for certified Quality levels*
 - Justification:
 - ❑ *Model users avoid wasted engineering cycles*
 - ❑ *Vendors become encouraged to join IBIS*

IBIS Proposal:
Require Logo Certification
for IBIS Quality Levels.



Bridging the Gap Between COM and IBIS-AMI

- How can the IBIS Open Forum help?
 - Quality:
 - *IBIS Quality currently provided as a specification*
 - *IBIS Quality is not enforced*
 - *IBIS Quality can be enforced*
 - Documentation: Model from Vendor or EDA-Library
 - *Model documentation is not part of IBIS IQ*
 - *IBIS Quality can also enforce documentation*
 - Solution:
 - *IBIS Logo-testing issued to model vendors*
 - *Potential for certified Quality levels*
 - Justification:
 - ❑ *Model users avoid wasted engineering cycles*
 - ❑ *Vendors become encouraged to join IBIS*

IBIS Proposal:
Require Logo Certification
for IBIS Quality Levels.

Did you know?
These organizations
require certification
testing to use their
official logos.
(Each uses COM and
IBIS-AMI)



Bridging the Gap Between COM and IBIS-AMI

- How can IBIS-AMI model users take action?
 - Quality:
 - *Require an IBIS IQ report from your vendor*
 - Standard: silicon is made to support at least one:
 - *Require Documentation: how to use model*
 - *EQ curves for TX and RX given test load*
 - *Require correlation to IEEE, OIF, USB, etc.*
 - Availability:
 - *First choice: get EQ values from COM*
 - *Second choice: get a reference AMI model for the standard the system requires*
 - *Vendor-specific model: should be 3rd choice*
 - Transferrable results B2B:
 - *Remind the legal department: IBIS is designed to be sharable performative model, with no IP.*

- IBIS member organizations registered during 2025:



Bridging the Gap Between COM and IBIS-AMI

- How else can IBIS-AMI model users take action?

- Quality:
 - *Get IBIS IQ reports from vendors as IBIS members*
- Availability:
 - *Some EDA vendors provide reference AMI models*
 - *Vendor-specific model: require user documentation*
- Engagement:
 - *Encourage your model vendor to join IBIS*
- Participate in IBIS activities and get latest news:
 - *Subscribe to IBIS email reflector*
 - *Join IBIS Open Forum and Task Group meetings*
- More information:
 - See the [IBIS website \(www.ibis.org\)](http://www.ibis.org)
 - Or find the IBIS Open Forum on social media

- IBIS member organizations registered during 2025:



(IBIS Members 2025: www.ibis.org/poster)

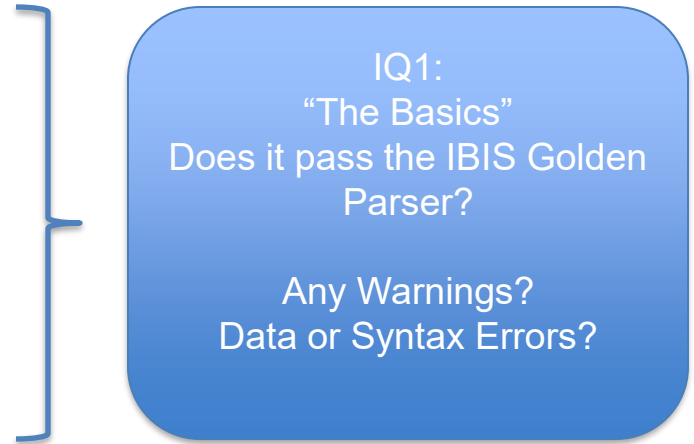


Bridging the Gap Between COM and IBIS-AMI

IBIS Quality Level Definitions:

The quality level is defined as a combination of correctness checks and correlation checks. The correctness level is a number, and other special designations such as correlation are shown as appended letters. Some examples:

- ✓ IQ0 - No IQ checking at all
- ✓ IQ1 - Passes IBISCHK without errors or unexplained warnings
- ❑ IQ2 - IQ1 + data for basic simulation checked
- ❑ IQ3 - IQ2 + data for timing analysis checked
- ❑ IQ4 - IQ3 + data for power-aware analysis checked
- ❑ IQ3M - IQ3 + correlated against hardware measurements
- ❑ IQ3MS - IQ3 + correlated against measurements and simulation
- ❑ IQ3GS - IQ3 + golden waveforms + correlated against simulation
- ❑ IQ4X - IQ4, but exception(s) to check(s) commented in file



The 5 recognized levels of correctness checks and 3 levels of correlation checks are discussed [in the specification].

Details of the referenced checks and correlation tests are given in sections 2 through 7 [in the specification].

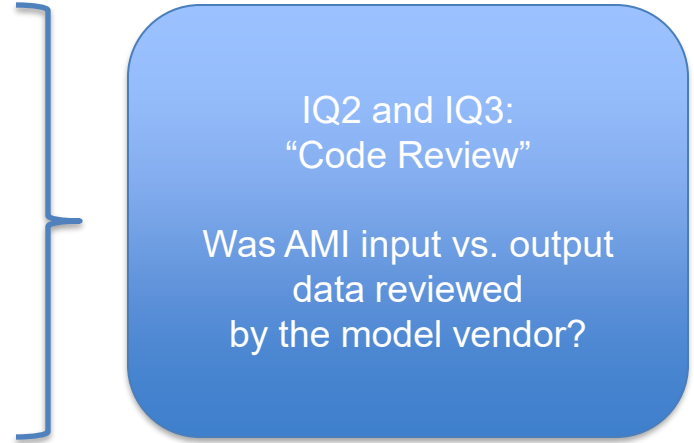


Bridging the Gap Between COM and IBIS-AMI

IBIS Quality Level Definitions:

The quality level is defined as a combination of correctness checks and correlation checks. The correctness level is a number, and other special designations such as correlation are shown as appended letters. Some examples:

- ✓ IQ0 - No IQ checking at all
- ✓ IQ1 - Passes IBISCHK without errors or unexplained warnings
- ✓ IQ2 - IQ1 + data for basic simulation checked
- ✓ IQ3 - IQ2 + data for timing analysis checked
- ❑ IQ4 - IQ3 + data for power-aware analysis checked
- ❑ IQ3M - IQ3 + correlated against hardware measurements
- ❑ IQ3MS - IQ3 + correlated against measurements and simulation
- ❑ IQ3GS - IQ3 + golden waveforms + correlated against simulation
- ❑ IQ4X - IQ4, but exception(s) to check(s) commented in file



The 5 recognized levels of correctness checks and 3 levels of correlation checks are discussed [in the specification].

Details of the referenced checks and correlation tests are given in sections 2 through 7 [in the specification].

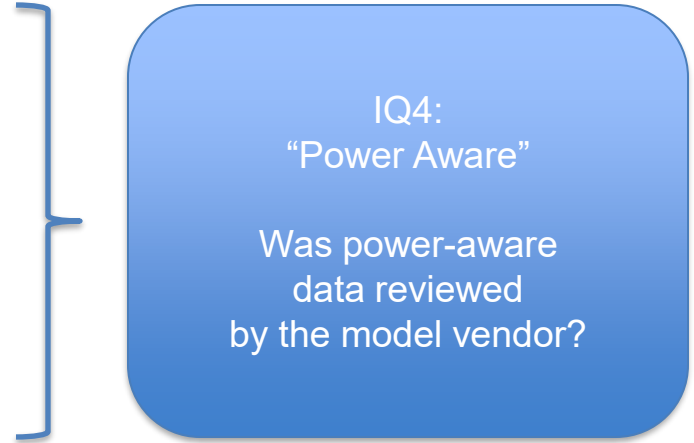


Bridging the Gap Between COM and IBIS-AMI

IBIS Quality Level Definitions:

The quality level is defined as a combination of correctness checks and correlation checks. The correctness level is a number, and other special designations such as correlation are shown as appended letters. Some examples:

- ✓ IQ0 - No IQ checking at all
- ✓ IQ1 - Passes IBISCHK without errors or unexplained warnings
- ✓ IQ2 - IQ1 + data for basic simulation checked
- ✓ IQ3 - IQ2 + data for timing analysis checked
- ✓ IQ4 - IQ3 + data for power-aware analysis checked
- ❑ IQ3M - IQ3 + correlated against hardware measurements
- ❑ IQ3MS - IQ3 + correlated against measurements and simulation
- ❑ IQ3GS - IQ3 + golden waveforms + correlated against simulation
- ❑ IQ4X - IQ4, but exception(s) to check(s) commented in file



The 5 recognized levels of correctness checks and 3 levels of correlation checks are discussed [in the specification].

Details of the referenced checks and correlation tests are given in sections 2 through 7 [in the specification].

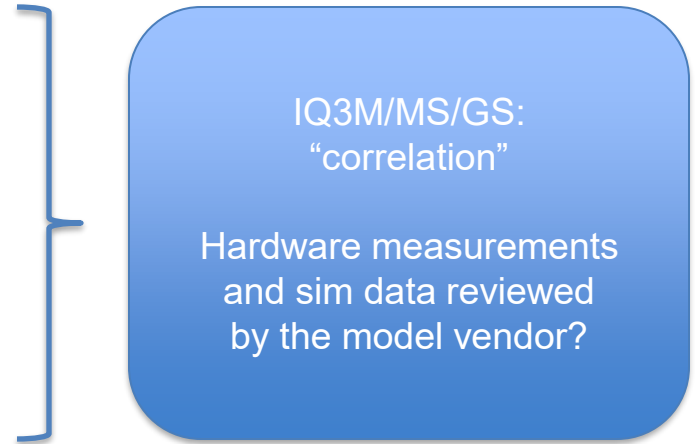


Bridging the Gap Between COM and IBIS-AMI

IBIS Quality Level Definitions:

The quality level is defined as a combination of correctness checks and correlation checks. The correctness level is a number, and other special designations such as correlation are shown as appended letters. Some examples:

- ✓ IQ0 - No IQ checking at all
- ✓ IQ1 - Passes IBISCHK without errors or unexplained warnings
- ✓ IQ2 - IQ1 + data for basic simulation checked
- ✓ IQ3 - IQ2 + data for timing analysis checked
- ✓ IQ4 - IQ3 + data for power-aware analysis checked
- IQ3M - IQ3 + correlated against hardware measurements
- IQ3MS - IQ3 + correlated against measurements and simulation
- IQ3GS - IQ3 + golden waveforms + correlated against simulation
- ❖ IQ4X - IQ4, but exception(s) to check(s) commented in file



The 5 recognized levels of correctness checks and 3 levels of correlation checks are discussed [in the specification].

Details of the referenced checks and correlation tests are given in sections 2 through 7 [in the specification].



Thank you!

QUESTIONS?

Graham Kus

Principal Signal and Power Integrity Engineer, Signal Edge Solutions, LLC

Graham@SignalEdgeSolutions.com | SignalEdgeSolutions.com



MORE INFORMATION

IEEE COM reference code in MATLAB®:

- IEEE 802.3 COM Open Source Project Ad Hoc:
- Link: (https://www.ieee802.org/3/ad_hoc/COM/index.html)

IEEE COM reference code in Python®:

- COM: A Link Designer's Field Guide, David Banas and Keysight Technologies, Inc., Signal Integrity Journal, June 2024.
- Ref: (<https://www.signalintegrityjournal.com/articles/3578-com-a-link-designers-field-guide>)
- GitHub: ([GitHub - Python implementation of COM, as per IEEE 802.3-22 Annex 93A.](#))

More topics on Signal and Power Integrity available at these links:

- www.SignalEdgeSolutions.com
- <https://www.signaledgesolutions.com/blog>
- Graham@SignalEdgeSolutions.com



Bridging the Gap: Channel Operating Margin (COM) and IBIS-AMI for Analysis and Development of High-Speed SerDes Designs

Graham Kus, Signal Edge Solutions, LLC

