Chair’s Status Report

Michael Mirmak
Intel Corp.
Chair, IBIS Open Forum

IBIS Summit
at DesignCon 2014
January 31, 2014

http://www.eda.org/ibis/
Agenda

- 2013 Events and Progress

- IBIS Development Data
  - BIRD and Version Life Cycles

- Planning for the Future
  - Is a 6-month Development Schedule Feasible?
  - Proposal for Discussion
  - Focus for 1H’14
Events and Progress in 2013

- IBIS has moved to a new “parent”: SAE, Inc.
- IBIS Ver. 6.0 approved Sept. 20, 2013
  - IBISCHK6 expected before June 2014
- 15 BIRDs or BIRD updates submitted
  - 7 approved, 10 rejected (includes BIRDs from 2010 and later)
- Asian and European Summits Continue
  - 280 attendees in Shanghai, Taipei, and Yokohama
  - IBIS in Europe continues with SPI-E 2013 in Paris
IBIS DEVELOPMENT DATA
How Long Do Versions “Live”? 

<table>
<thead>
<tr>
<th>IBIS Version</th>
<th>Days Between Approval and Successor</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>532</td>
</tr>
<tr>
<td>2.1</td>
<td>637</td>
</tr>
<tr>
<td>3</td>
<td>602</td>
</tr>
<tr>
<td>3.1</td>
<td>532</td>
</tr>
<tr>
<td>3.2</td>
<td>602</td>
</tr>
<tr>
<td>4</td>
<td>637</td>
</tr>
<tr>
<td>4.1</td>
<td>602</td>
</tr>
<tr>
<td>4.2</td>
<td>637</td>
</tr>
<tr>
<td>5</td>
<td>1456</td>
</tr>
<tr>
<td>5.1</td>
<td>307</td>
</tr>
<tr>
<td>6</td>
<td>0</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>Average</th>
<th>Min</th>
<th>Max</th>
<th>STD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Days</td>
<td>652</td>
<td>133</td>
<td>1456</td>
<td>357</td>
</tr>
</tbody>
</table>
How Long are BIRDS Considered?

- Average in 2012: 134 days
- “Statutory” minimum is now 14 days

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<thead>
<tr>
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<th></th>
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</thead>
<tbody>
<tr>
<td><strong>Average</strong></td>
<td>137.01</td>
</tr>
<tr>
<td><strong>Min</strong></td>
<td>3</td>
</tr>
<tr>
<td><strong>Max</strong></td>
<td>1001</td>
</tr>
<tr>
<td><strong>STD</strong></td>
<td>161.72</td>
</tr>
</tbody>
</table>
Pending BIRDs

- 10 BIRDs are open, mostly package-related

![Bar Chart]

- Longer than any other BIRD
- Longer than any IBIS version other than 5.0!

2014 IBIS Summit at DesignCon
Is a 6-month Schedule Feasible?

- Recall the 6-month development schedule (see Backup)
  - 4 months of technical discussion
  - 2 months of editing
  - Approval votes in May and Nov. of each year

- To maintain this schedule, close all BIRDs by March 1
  - May 16th approval vote for next IBIS version
  - IBIS 6.0 has 50 days left on its 6-month “life”
  - Is this sensible with a ~6 month parser development schedule?
  - Is absorption time of IBIS by industry actually 1 year or more?

- Which version of IBIS is the next one?
  - IBIS 6.1 means new parser development would be free to licensees (expensive for Open Forum)
  - IBIS 7.0 means raising new fees from licensees
  - Date-based versioning is a proposed alternative
Proposal for Discussion

- Move to combined membership + parser contribution model in 2015
  - Membership fee would include an amount specifically for parser software development

- Move to new version numbering at same time
  - Remove distinction between major and minor releases based on features
  - Date-based version accommodates this
  - Standardization may take place at any time

- Proposal: make next version 6.1
  - Keywords/features in IBIS *per se* likely minimal
  - IBIS-ISS parsing becomes a focus
Looking Ahead

- Focus areas for 1H 2014
  - Resolve all packaging BIRDs and finalize packaging direction for IBIS as a modeling approach
- Return to IBIS-ISS parser
  - IBIS-ISS a focus under all today’s package proposals
  - Call for parser bids ready but potentially stale
- Once IBIS updates are complete…
  - New charter and elections schedule under SAE

Thanks for another great year!
Backup
Release Schedule for IBIS

- Moving to a regular, software-like release plan
  - Allows better planning by “customers” (IBIS community)
  - Keeps number of changes manageable
  - Reduces & stabilizes parser development cycle time and cost

- Two “cycles”, each with two parts
  - Cycle 1
    - Nov. – Feb.: technical development
    - March 1 – close technical changes
    - May 1 – close final document (editorial fixes in-between)
  - Cycle 2
    - May – Aug.: technical development
    - Sept. 1 – close technical changes
    - Nov. 1 – close final document (editorial fixes in-between)
How Does This Work?

Parser development can start as early as this point

<table>
<thead>
<tr>
<th>JAN</th>
<th>FEB</th>
<th>MAR</th>
<th>APR</th>
<th>MAY</th>
<th>JUN</th>
<th>JUL</th>
<th>AUG</th>
<th>SEP</th>
<th>OCT</th>
<th>NOV</th>
<th>DEC</th>
</tr>
</thead>
<tbody>
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Cycle 1

- Editorial Work
- BIRD Development

Cycle 2

- Editorial Work
- BIRD Development

Document votes & releases here
## IBIS Post-6.0 BIRDs So Far

<table>
<thead>
<tr>
<th>ID#</th>
<th>Issue Title</th>
<th>Requester</th>
<th>Submitted</th>
<th>Days Open</th>
</tr>
</thead>
<tbody>
<tr>
<td>125.1</td>
<td><strong>Make IBIS-ISS Available for IBIS Package Modeling</strong></td>
<td>Arpad Muranyi, Mentor Graphics</td>
<td>21-Oct-10</td>
<td>1198</td>
</tr>
<tr>
<td>128</td>
<td><strong>Allow AMI_parameters_out to pass AMI_parameters_in data on calls to AMI_GetWave</strong></td>
<td>Walter Katz, SiSoft</td>
<td>11-Mar-11</td>
<td>1057</td>
</tr>
<tr>
<td>147</td>
<td><strong>Back-channel support</strong></td>
<td>Marcus Van Ierssel, Snowbush IP; Kumar Keshavan, Sigrity, Inc.; Ken Willis, Sigrity, Inc.; Walter Katz, SiSoft</td>
<td>18-Oct-11</td>
<td>836</td>
</tr>
<tr>
<td>157</td>
<td><strong>Parameterize [Driver Schedule]</strong></td>
<td>Arpad Muranyi, Mentor Graphics; Romi Mayder, Xilinx</td>
<td>24-Jan-13</td>
<td>372</td>
</tr>
<tr>
<td>158.3</td>
<td><strong>AMI Touchstonefile (R) Analog Buffer Models</strong></td>
<td>Walter Katz, Signal Integrity Software, Inc.</td>
<td>20-Feb-13</td>
<td>345</td>
</tr>
<tr>
<td>161.1</td>
<td><strong>Supporting Incomplete and Buffer-only [Component] Descriptions</strong></td>
<td>Michael Mirmak, Intel Corp.</td>
<td>8-May-13</td>
<td>268</td>
</tr>
<tr>
<td>163</td>
<td><strong>Instantiating and Connecting [External Circuit] Package Models with [Circuit Call]</strong></td>
<td>Arpad Muranyi &amp; John Angulo, Mentor Graphics; Ambrish Varma &amp; Brad Brim, Cadence Design Systems, Inc.</td>
<td>9-Jan-14</td>
<td>22</td>
</tr>
<tr>
<td>164</td>
<td><strong>Allowing Package Models to be defined in [External Circuit]</strong></td>
<td>Ambrish Varma &amp; Brad Brim, Cadence Design Systems, Inc.; Arpad Muranyi, Mentor Graphics</td>
<td>9-Jan-14</td>
<td>22</td>
</tr>
</tbody>
</table>
Q/A