More on IBIS Modeling for Load-Dependent Current-Mode Differential Drivers

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Outlines

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- Load-Dependent Current-Mode Differential Driver
- IBIS Extraction Method
 - Legacy method
 - Enhanced extraction method
- Summary

Note: The partial materials in this presentation have been presented in 2013 European IBIS Summit in Paris

Motivation

- Load-dependent current-mode differential pair buffers are found increasingly often in GHz serial link designs
 - Easy to control the output current and common-mode voltage
 - Often used in pre-emphasis buffers
- Using traditional IBIS buffer extraction method is not accurate enough

Is it possible to use the current IBIS spec for this kind of differential buffer?

Load-Dependent Current-Mode Differential Driver



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Load-Dependent Current-Mode Differential Driver (Current Flow)



IBIS Extraction Method (Modeling as 2 individual pins)

No load to be used for I-V curve extraction



Pictures from IBIS cookbook

IBIS Extraction Method (Modeling as 2 individual pins)

Load connected to GND or VCC to be used for V-T curve extraction



Figure 3.2 – Simulation Setup for Extracting Ramp Rate Information (Rising Edge Shown) Pictures from IBIS cookbook

Using normal extraction methods for I-V and V-T curves





Correlation shows they are way off from the Spice result RED – Spice, BLUE - IBIS

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Enhanced I-V Extraction Method



Using enhanced extraction method with differential load

* I-V curves shown are combined curves and load line using reference to GND





Correlation shows matched result with Spice RED – SPICE, BLUE - IBIS



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Summary

- Normal IBIS extraction method for load-dependent current-mode differential pair buffers produces inaccurate models
- Enhanced method considering differential load can solve this issue. It gives the matched results when correlating with Spice simulation results
- It would be better to have IBIS Spec accept "Rref_diff/Cref_diff" kind of differential loads for regular IBIS differential pair models
 - Rref_diff/Cref_diff is limited for External model use now
- IBIS Spec needs to be enhanced when modeling dynamic PLL current mode buffer
 - Various I-V tables for different diff_loads
 - Current dependent C-comp value table

