**AGENDA - Asian IBIS Summit (Taipei)**

**Wednesday, November 15, 2017**

Sherwood Hotel

Taipei, Taiwan

Room: **3F**

Sponsors: **IO Methodology**

**Peace Giant Company (SPISim)**

**ZTE Corporation**

*(Order and times subject to change)*

8:15 **REFRESHMENTS AND SIGN IN**

8:45 **WELCOME**

* Mike LaBonte (Chair, IBIS Open Forum)
(SiSoft, USA)

9:15 **IBIS Update**

Mike LaBonte (SiSoft, USA)

9:45 **IBIS Interconnect Modeling Using IBIS-ISS and Touchstone**

Michael Mirmak (Intel Corporation, USA)

*[Presented by Mike LaBonte (SiSoft, USA)]*

10:20 **Leveraging IBIS Capabilities for Multi-Gigabit Interfaces**

Ken Willis (Cadence Design Systems, USA)

[Presented by Skipper Liang (Cadence Design Systems PRC)]

10:50 **BREAK ( REFRESHMENTS AND VENDOR TABLES )**

* Reconvene at 11:10

11:20 **Comparison of Time Domain and Statistical IBIS-AMI Analyses**

Mike LaBonte (SiSoft, USA)

12:00 **FREE BUFFET LUNCH ( Hosted by Sponsors )**

* Vendor tables. Reconvene at 13:30

13:30 **Characterizing and Modeling of a Linear CTE**

Skipper Liang (Cadence Design Systems, ROC)

14:10 **Using DATA Files for IBIS-AMI Models**

Lance Wang (IO Methodology, USA)

14:40 **IBIS-AMI Modeling Using Scripts and Spice Models**

Wei-hsing Huang (SPISim, USA)

15:10 **BREAK ( Refreshments and Vendor Tables )**

* Reconvene at 15:30

16:20 **OPEN DISCUSSION**

17:20 **CONCLUDING ITEMS**

17:30 **END OF IBIS SUMMIT MEETING**

* Thank you for your participation

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