



ASIAN IBIS Summit Dec 2005 Slideshow

**Syed Huq
Cisco Systems, Inc**

Contributors

Cisco.com

- **Syed Huq**
- **Michael Mirmak**
- **Vivian Pan**
- **Ke Xu**

The setup..



ASIAN IBIS SUMMIT

—亚洲IBIS技术研讨会

会议地点： 威尼斯二号宴会厅(一楼)

ASIAN IBIS SUMMIT

亚洲IBIS技术研讨会



December 6, 2005
Crowne Plaza Hotel
Shenzhen, CHINA

2005年12月6日
威尼斯酒店
中国 深圳



HUAWEI TECHNOLOGIES

cadence

SiSoft

Mentor
Graphics

SIGRITY

ELA IBIS OPEN FORUM
<http://www.eigroup.org/ibis/>







Attendees..

ASIAN IBIS SUMMIT — 亚洲IBIS技术研讨会





Presentors..



ASIAN IBIS SUMMIT

亚洲 IBIS 技术研讨会

December 6, 2005
Shenzhen, P. R. China

2005 12 6

IBIS

cadence

SiSoft



ASIAN IBIS SUMMIT

亚洲 IBIS 技术研讨会

December 8, 2005
Shenzhen, P. R. China

2005 11 28



Cadence

DiSoft



ASIAN IBIS
SUMMIT

IBIS 技术研讨会

December 4, 2003
Singapore, P. R. China

2003

IBIS
SOCIETY



Overview

IBIS Models have played an important role
in signal integrity analysis

- Access Buffer Characteristics
- Waveform Quality Check
- Timing Analysis



Loose Timing Budget System Analysis

- Definition
- Required inputs
 - Timing topology
 - Component-level timing
 - Operating clock frequency
 - Approximated flight time

Nov 20, 2012



What are the Objectives of the SI Engineer?

- Maximize performance
 - Highest possible clock speed
- Ensure reliable operation
 - Meet timing constraints
 - Keep overhead below levels that will cause device malfunction or destruction.
 - Maintain adequate switching margins
- Minimize per unit costs
 - Minimize the number of components (including terminators)
 - Use lowest cost PCB materials

JEITA EDA -WG Activity and Study of Interconnect Model

Dec 6, 2005

Asian IBIS SUMMIT in

JEITA EDA

Takeshi Watanabe (JEITA)

JEITA - Japan Electronics and Information Technology

© JEITA 2005 All Rights Reserved



Lunch with the Press..

















IBIS and Power Delivery Systems

Jiang, Xiangzhong

Li, Jinjun

Zhang, Shengli

Huawei Technologies

HUAWEI TECHNOLOGIES





SIGRITY

Power Delivery System, Signal Return Path, and Simultaneous Switching Output Analysis Guidelines

Asia IBIS Summit

Raymond Y. Cheung

Sam Chittwood

Sigrity, Inc.

November 14, 2013

Splitting C_comp for Power Integrity Simulations

Zhiping Yang, Ph.D.
Cisco Systems, Inc.
(Now with Apple Computer)

Asian IBIS Summit in Singapore
12/6/2005



IBIS在信号完整性分析中的应用 Applying IBIS for SI Analysis

Wang, ZhangMin Zhong
ce Design Systems, Inc.
IBIS Summit, Shen Zhen, P.R. Chi
ber 4, 2005



Modeling methodology

- Modeling is quite involved, it covers active devices as well as passive devices, such as package, transmission line, connector, via, and plane etc.
- Not all modeling methods are the same. They have tradeoffs and are suitable for different applications.
- There are behavioral IBIS and structural Spice models for active devices. Spice model is applicable in demanding situations, while IBIS model is used in system and board level simulation.
- Circuit simulators can run both IBIS and Spice models. Simulators have different characteristics.



SiSoft

IBIS Models for DDR2 Analysis
Asian IBIS Summit 2005

Barry Katz, President and CEO



HITACHI
Inspire the Next

Practical Measurement vs. Simulation
Correlation with DDR2 667 Interface

Kazuyoshi Shoji

Hitachi ULSI System



Improving IBIS ECL Algorithms
改进 IBIS ECL 算法

Bob Ross
Asian IBIS Summit
Shenzhen, CHINA
December 6, 2005

亚洲

Breaks and Demos..







nce

evidence

EM
T
EM

技术

成就全球
电子产品
软件、硬
费电子产
半导体器
收入约12
美国加州
设计中

市场，迄
。在过去
有员工约
港五个办
书中心



The End