
IBIS SUMMIT MEETING
Thursday, February 3, 2011

Great America 3
(second floor)
Santa Clara Convention Center

Presentations at <http://www.eda.org/ibis/summits/>

Co-sponsored by the IBIS Open Forum and DesignCon

- 8:00 AM Refreshments & Sign In
- 8:30 AM Call to Order
- Welcome to Summit
- Introductions
- Opens for Issues, Discussion Topics
- 8:45 AM **IBIS Chair's Report**
Michael Mirmak, Intel Corporation
- 9:00 AM **[External Test Load/Data] Concepts**
Anders Ekholm* and Mike LaBonte**
*Ericsson and **Cisco Systems
- 9:30 AM **IBIS-ISS Introduction and Futures**
Michael Mirmak, Intel Corporation
- 10:00 AM BREAK
- 10:15 AM **Reflections on S-parameter Quality**
Yuriy Shlepnev, Simberian
- 11:00 AM **T-Coil Topics**
Bob Ross, Teraspeed Consulting Group
- 11:30 AM **IBIS-ATM Task Group Report**
Arpad Muranyi, Mentor Graphics Corporation

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- 11:45 AM LUNCH
Pre-registration required
- 1:00 PM **Modeling Analog Repeaters in IBIS-AMI**
Walter Katz and Mike Steinberger
Signal Integrity Software (SiSoft)
- 1:30 PM **IBIS-AMI Analog Modeling and Much Needed Improvements for IBIS**
Arpad Muranyi, Mentor Graphics Corporation
- 2:00 PM **Extending IBIS-AMI to Enable Back-channel Communication**
Kumar Keshavan*, Marcus Van Ierssel**, and Ken Willis*
*Sigrity and **Gennum
- 2:30 PM BREAK
- 2:45 PM **AMI Backchannel Co-Optimization**
Walter Katz, Signal Integrity Software (SiSoft)
- 3:15 PM **Open Discussion and Ad Hoc Presentations**
- 4:45 PM Concluding Items
- Next Open Forum Meeting: February 18, 2011
- 5:00 PM End of Meeting Room Availability