
AGENDA - EPEPS IBIS SUMMIT MEETING

Wednesday, October 28, 2015

DoubleTree by Hilton Hotel
2050 Gateway Place
San Jose, California
Room: Monterey

Sponsors:

EPEPS
Keysight Technologies
Mentor Graphics Corporation
Synopsys

- 1:00 PM REFRESHMENTS AND SIGN IN**
- 1:15 PM OFFICIAL OPENING**
- Welcome to Summit
- Introductions
- 1:25 PM IBIS Chair's Report**
Mike LaBonte, Signal Integrity Software (SiSoft)
- 1:40 PM SPI 2016 20th IEEE Workshop on Signal and Power Integrity**
Stefano Grivet-Talocia, Politecnico di Torino
- 1:55 PM Turin and its Province (Movie)**
Stefano Grivet-Talocia, Politecnico di Torino
- 2:00 PM SAE, ITC and IBIS**
Logen Johnson, SAE International
- 2:15 PM Enhanced Macromodels for I/O Buffers**
Gianni Signorini, Intel Corporation, University of Pisa;
Claudio Siviero, Igor Simone Stievano, and
Stefano Grivet-Talocia, Politecnico di Torino
- 2:45 PM Accurate Statistical Analysis of High-Speed Links with PAM-4 Modulation**
Vladimir Dmitriev-Zdorov, Mentor Graphics Corp
- 3:15 PM IBIS Interconnect BIRD Update**
Walter Katz, Signal Integrity Software (SiSoft)
- 3:45 PM BREAK**
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**4:00 PM Some Results for General K-table Extraction Proposal
Using SPICE**

Bob Ross, Teraspeed Labs;
Xuefeng Chen, Synopsys

4:30 PM IBIS-AMI Modelling of High-Speed Memory Interfaces

John Yan and Arash Zargaran-Yazd, Rambus, Inc.

**5:00 PM ibisami - An Open Source, Public Domain IBIS-AMI
Model Creation Infrastructure**

David Banas, eASIC

5:30 PM Introducing IBIS Version 6.1

Michael Mirmak
Intel Corporation

5:50 PM DISCUSSION AND CLOSING REMARKS

- Next Open Forum Meeting: November 20, 2015

6:00 PM END OF MEETING
